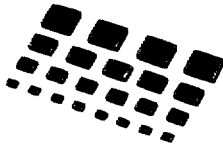


SURFACE MOUNT MONOLITHIC CHIP CAPACITORS HIGH DIELECTRIC CONSTANT TYPE



SURFACE MOUNT
MONOLITHIC CHIP
CAPACITORS

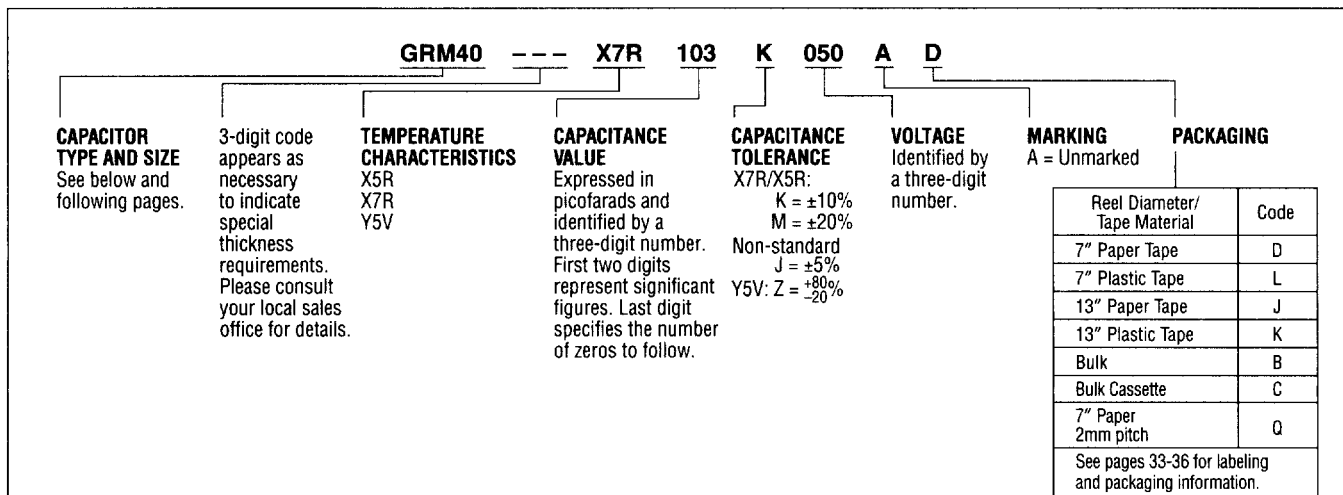
GRM36/39/40/42-6/42-2/43-2/44-1 Series



FEATURES

- Miniature size
- No Polarity
- Nickel Barrier Termination Standard – highly resistant to metal migration
- Uniform dimensions and configuration
- Suitable for reflow soldering
- GRM39, 40 and 42-6 suitable for wave soldering
- Minimum series inductance
- Tape and Reel Packaging
- Bulk Case Packaging available for GRM40 and smaller
- Wide selection of capacitance values and voltages
- Largest production capacity and volume in the world

PART NUMBERING SYSTEM



CHIP DIMENSIONS

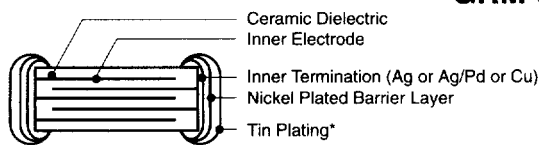
Dimensions: mm	Size	EIA Code	L Length	W Width	T Thickness	e (min.) Termination	g (min.) Insulation
	GRM36	0402	1.0 ± 0.05	0.5 ± 0.05	0.5 ± 0.05	0.15 - 0.3	0.4
	GRM39*	0603	1.6 ± 0.1	0.8 ± 0.1	0.8 ± 0.1	0.2 - 0.5	0.5
	GRM40	0805	2.0 ± 0.1	1.25 ± 0.1	0.6 ± 0.1	0.2 - 0.7	0.7
					0.85 ± 0.1		
					1.25 ± 0.1		
	GRM42-6	1206	3.2 ± 0.15	1.6 ± 0.15	0.85 ± 0.1	0.3 - 0.8	1.5
					1.15 ± 0.1		
	GRM42-2	1210	3.2 ± 0.3	2.5 ± 0.2	1.6 ± 0.2	0.3 min.	1.0
					1.15 ± 0.1		
					1.35 ± 0.15		
GRM43-2	1812	4.5 ± 0.4	3.2 ± 0.3	1.8 ± 0.2	0.3 min.	2.0	
				2.5 ± 0.2			
GRM44-1	2220	5.7 ± 0.4	5.0 ± 0.4	2.0 max.	0.3 min.	2.0	

*Bulk case packaging is L = 1.6 ± 0.07, W, T = 0.8 ± 0.07.

CHIP TERMINATION DIAGRAMS

Nickel Barrier Layer (Standard)

GRM Series



*Size 0402 – Solder Plated



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SURFACE MOUNT MONOLITHIC CHIP CAPACITORS HIGH DIELECTRIC CONSTANT TYPE- SPECIFICATION

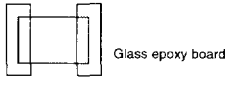
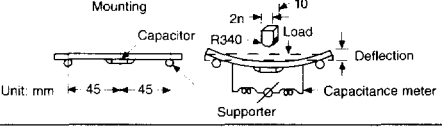


GRM36/39/40/42-6/42-2/43-2/44-1 Series

GENERAL/ELECTRICAL

Capacitance Change with Temperature:	X5R: $\pm 15\% \Delta CX$ $-55^{\circ}C$ to $+85^{\circ}C$ X7R: $\pm 15\% \Delta CX$ $-55^{\circ}C$ to $+125^{\circ}C$ Y5V: $^{+22}_{-82}\% \Delta CX$ $-30^{\circ}C$ to $+85^{\circ}C$	Insulation Resistance (I.R.)	X5R/X7R 100,000 megohms or 1000 megohms-mfd (whichever is less) Y5V 10,000 megohms or 500 megohms-mfd (whichever is less)																				
Capacitance & D.F. (Frequency & Voltage)	X5R, X7R: 1kHz $\pm 100Hz$ @ 1.0 $\pm 2V_{rms}$ Y5V: 1kHz $\pm 100Hz$ @ 1.0 $\pm 2V_{rms}$	Dielectric Strength (Flash)	250% of rated voltage for 5 seconds with series resistor limiting charge current to 50mA max.; 200% for 500V																				
Dissipation Factor (D.F.)	<table border="1"> <tr> <td></td> <td>Min. 25V</td> <td>16V</td> <td>10V</td> <td>6.3V</td> </tr> <tr> <td>X5R</td> <td>2.5%</td> <td>3.5%</td> <td>3.5%</td> <td>5%</td> </tr> <tr> <td>X7R</td> <td>2.5%</td> <td>3.5%</td> <td>3.5%</td> <td>5%</td> </tr> <tr> <td>Y5V</td> <td>5.0%</td> <td>9.0%</td> <td>12.5%</td> <td>12.5%</td> </tr> </table>		Min. 25V	16V	10V	6.3V	X5R	2.5%	3.5%	3.5%	5%	X7R	2.5%	3.5%	3.5%	5%	Y5V	5.0%	9.0%	12.5%	12.5%	Typ. Aging (per Decade)	X5R/X7R 3% Y5V 7%
	Min. 25V	16V	10V	6.3V																			
X5R	2.5%	3.5%	3.5%	5%																			
X7R	2.5%	3.5%	3.5%	5%																			
Y5V	5.0%	9.0%	12.5%	12.5%																			

MECHANICAL

TEST	TEST METHOD	POST TEST LIMITS
Terminal Adhesion		<0603 1.0 lbs. ≥0805 2.2 lbs. No evidence of termination peeling
Deflection		1 mm deflection (Glass epoxy board) No mechanical damage Cap., DF, IR meet initial limits
Solderability	MIL-STD-202 Method 208F	Meets Requirement For specific details contact factory

ENVIRONMENTAL

TEST	TEST METHOD	POST TEST LIMITS
Thermal Shock (Air to Air)	MIL-STD-202, Method 107, Condition A Prior to starting Thermal Shock test, capacitors shall be heat treated (deaged) for one (1) hour at 150°C. Allow capacitors to stabilize at room temperature for 48 hours prior to taking initial measurements. Post thermal Shock measurement shall be taken after 48 hours stabilization.	Appearance: No visual damage ΔC : X5R/X7R = $\pm 12.5\%$ Y5V = $\pm 30.0\%$ D.F.: X5R/X7R = 2.5% max. @ 25°C, (3.5% max. @ 25°C for 16V & 10V Series) (7.5% max. @ 25°C for 6.3V Series) Y5V = 5.0% max. @ 25°C, (9.0% max. @ 25°C for 16V Series) (15% max. @ 25°C for 10V & 6.3V Series) I.R.: X5R/X7R = 100,000 Ω min. of 1,000M $\Omega \cdot \mu F$ (whichever is less) Y5V = 10,000 Ω or 500M $\Omega \cdot \mu F$ min. (whichever is less)
Humidity, Steady State	Maintain the capacitor at 40 $\pm 2^{\circ}C$ and 90 to 95% humidity for 500 ± 12 hours. Remove and let sit for 48 ± 4 hours at room temperature, then measure.	Appearance: No defects Capacitance: X5R, X7R within $\pm 12.5\%$; Z5U, Y5V within $\pm 30\%$ Q/D.F.: See chart below. I.R.: 1,000M Ω or 50 Ω F (whichever is less)
Humidity Load	Apply the rated voltage at 40 $\pm 2^{\circ}C$ and 90 to 95% humidity for 500 ± 12 hours. Remove and let sit for 48 ± 4 hours at room temperature, then measure. The charge/discharge current is less than 50mA. • Initial measurement for Y5V/10V max. Apply the rated DC voltage for 1 hour at 40 $\pm 2^{\circ}C$. Remove and let sit for 48 ± 4 hours at room temperature. Perform initial measurement.	Appearance: No defects Capacitance: X5R, X7R within $\pm 12.5\%$; Z5U within $\pm 30\%$; Y5V within +30/-40% (10Vmax), within $\pm 30\%$ (others)
Life Test	Apply 200% of rated voltage for 1000 ± 12 hours at maximum operating temperature; 150% for 500V Upon completion of above test wait 48 hours prior to performing post testing.	I.R.: 500M Ω or 25 Ω F (whichever is less) Dielectric Strength: No failure

Q/D.F.	Char.	25V min.	16V	10V	6.3V
	X5R X7R	0.05 max.	0.05 max.	0.05 max.	0.075 max.
Z5U	0.05 max.	—	—	—	
Y5V	0.075 max.	0.1 max. (C<1.0 μF) 0.125 max. (C≥1.0 μF)	0.15 max.	0.15 max.	

SURFACE MOUNT MONOLITHIC CHIP CAPACITORS HIGH DIELECTRIC CONSTANT X5R/X7R/Y5V TYPES



GRM36/39/40 Series

SURFACE MOUNT
MONOLITHIC CHIP
CAPACITORS

Type (EIA Code)	GRM36* (0402)										GRM39 (0603)										GRM40 (0805)											
	Char.		X5R		X7R			Y5V			X7R				Y5V			X7R					Y5V									
Cap. (pF)	Volt.	10	16	10	16	25	50	10	16	25	50	10	16	25	50	100	200	10	16	25	50	100	10	16	25	50	100	200	16	25	50	100
220																																
270																																
330																																
390																																
470																																
560																																
680																																
820																																
1000																																
1200																																
1500																																
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820000																																
1000000																																
1500000																																
2200000																																
3300000																																
4700000																																
10000000																																

*GRM36 Series is suited to only reflow soldering.
¹Type: GRM40-034 (L: 2 ± 0.15, W: 1.25 ± 0.15, T: 1.25 ± 0.15)
²Only for taping

THICKNESS AND PACKAGING TYPES/QUANTITY

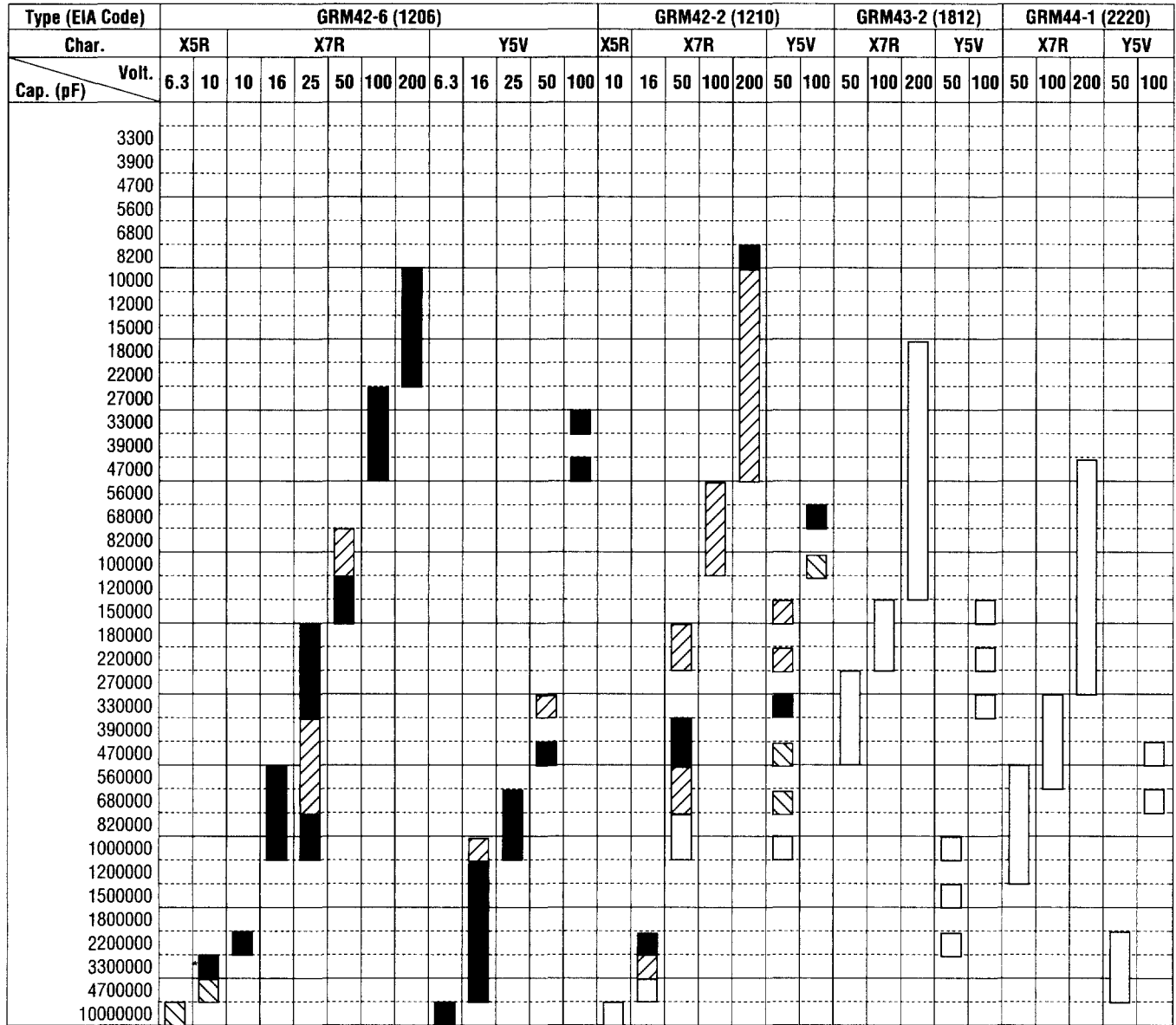
Type	Thickness: T (mm)	Bulk (pcs./bag)	Taping (pcs./φ178mm reel) ³	Bulk Case (pcs./case)	Type	Thickness: T (mm)	Bulk (pcs./bag)	Taping (pcs./φ178mm reel) ³	Bulk Case (pcs./case)
GRM36	□: 0.5 ± 0.05	1000	10000	50000	GRM40	▨: 0.85 ± 0.1	1000	4000	—
GRM39	□: 0.8 ± 0.1 ⁴	1000	4000	15000		■: 1.25 ± 0.1	1000	3000	5000
GRM40	□: 0.6 ± 0.1	1000	4000	10000					

³φ330mm reel is available on request. ⁴Bulk case packaging is T = 0.8 ± 0.07.

SURFACE MOUNT MONOLITHIC CHIP CAPACITORS HIGH DIELECTRIC CONSTANT X5R/X7R/Y5V TYPES



GRM42-6/42-2/43-2/44-1 Series



*Type: GRM42-631 (L: 3.2 ± 0.2, W: 1.6 ± 0.2, T: 1.3⁺⁰_{-0.2})

THICKNESS AND PACKAGING TYPES/QUANTITY

Type	Thickness: T (mm)	Bulk (pcs./bag)	Taping (pcs./φ178mm reel) ¹	Type	Thickness: T (mm)	Bulk (pcs./bag)	Taping (pcs./φ178mm reel) ¹
GRM42-6	: 0.85 ± 0.1	1000	4000	GRM42-2	: 1.8 ± 0.2	1000	1000
	: 1.15 ± 0.1	1000	3000		: 2.5 ± 0.2	1000	1000
	: 1.6 ± 0.2	1000	2000		GRM43-2	: 2.0 max.	1000
GRM42-2	: 1.15 ± 0.1	1000	3000	GRM44-1	: 2.0 max.	1000	1000
	: 1.35 ± 0.15	1000	2000		*φ330mm reel is available on request.		

Note: Capacitance Values = EIA 12 step: X7R = 10,12,15,18,22,27,33,39,47,56,68,82
6 step: Y5V = 10,15,22,33,47,68. For other values contact your local Murata Sales Office

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[Home](#) > [Product Information](#) > [Search Engine](#) > [Catalog](#) > [GRM39X5R105K6.3](#)

Capacitors > Monolithic Ceramic Capacitors > for Flow/Reflow Soldering
















Specification

Part Number	GRM39X5R105K6.3
Length L	1.60mm ±0.10mm
Width W	0.80mm ±0.10mm
Thickness T	0.80mm ±0.10mm
Electrode e	0.20 to 0.50mm
Electrode Gap g	0.50mm min.
TC Code	X5R
Capacitance Change	±15%
TC Change Correction	
Temperature Range	-55 to 85°C
Capacitance	1.0μF ±10%
Rated Voltage	6.3Vdc
Soldering Method	FLW / RFLW
Recommended Parts	
EIA	0603

Minimum Quantity

180mm Paper Tape	4000
180mm Plastic Tape	
330mm Paper Tape	10000
330mm Plastic Tape	
Bulk Case	
Bulk(Bag)	1000
Flat Pack	
335Reel	
Magazine	
Box	

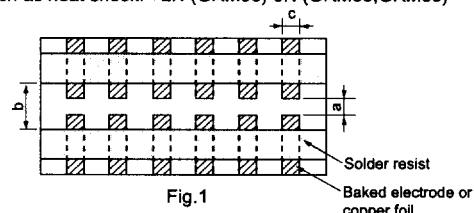
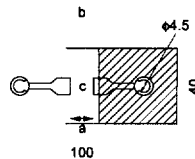
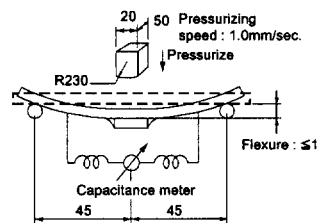
Details

-  [Appearance](#)
-  [Dimensions](#)
-  [Feature & Applctn](#)
-  [Cap.-Temp.Char.](#)
-  [Cap.Chng.-Aging](#)
-  [Impdnc.-Freq.Char.](#)
-  [Notice \(storage and operation condition\)](#)
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-  [Notice \(soldering and mounting\)](#)
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-  [Notice \(other\)](#)
-  [Specifications](#)
-  [Packaging](#)
-  [Information for Data](#)
-  [Sample Kit](#)

Product specifications in this catalog are as of Fed. '00, and are subject to change or obsolescence

■ Specifications and Test Methods

No.	Item	Specification		Test Method																								
		Temperature Compensating Type	High Dielectric Type																									
1	Operating Temperature	-55 to +125°C	X5R : -55 to +85°C X7R : -55 to +125°C Z5U : +10 to +85°C Y5V : -30 to +85°C																									
2	Rated Voltage	See the previous page.		The rated voltage is defined as the maximum voltage which may be applied continuously to the capacitor. When AC voltage is superimposed on DC voltage, V^{*P} or V^{OP} , whichever is larger, shall be maintained within the rated voltage range.																								
3	Appearance	No defects or abnormalities.		Visual inspection.																								
4	Dimensions	Within the specified dimensions.		Using calipers on micrometer.																								
5	Dielectric Strength	No defects or abnormalities.		No failure shall be observed when *300% of the rated voltage (C0Δ to U2J and SL) or *250% of the rated voltage (X5R, X7R, Z5U and Y5V) is applied between the terminations for 1 to 5 seconds, provided the charge/discharge current is less than 50mA. *200% for 500V																								
6	Insulation Resistance	More than 10,000MΩ or 500Ω · F (Whichever is smaller)		The insulation resistance shall be measured with a DC voltage not exceeding the rated voltage at 25°C and 75%RH max. and within 2 minutes of charging.																								
7	Capacitance	Within the specified tolerance.		The capacitance/Q/D.F. shall be measured at 25°C at the frequency and voltage shown in the table.																								
8	Q/ Dissipation Factor (D.F.)	30pFmin. : Q ≥ 1000 30pFmax. : Q ≥ 400+20C C : Nominal Capacitance (pF)	[X5R,X7R] W.V. : 25Vmin. : 0.025max. W.V. : 16/10V : 0.035max. W.V. : 6.3V 0.05max.(C<3.3μF) 0.1max.(C≥3.3μF)	<table border="1"> <thead> <tr> <th>Item</th> <th>Char.</th> <th>Frequency</th> <th>Voltage</th> </tr> </thead> <tbody> <tr> <td>C0Δ to U2J, SL (1000pF and below)</td> <td></td> <td>1±0.1MHz</td> <td>0.5 to 5Vrms</td> </tr> <tr> <td>C0Δ to U2J, SL (more than 1000pF)</td> <td></td> <td>1±0.1kHz</td> <td>1±0.2Vrms</td> </tr> <tr> <td>X5R, X7R, Y5V (10μF and below)</td> <td></td> <td>1±0.1kHz</td> <td>1±0.2Vrms</td> </tr> <tr> <td>X5R, X7R, Y5V (more than 10μF)</td> <td></td> <td>120±24Hz</td> <td>0.5±0.1Vrms</td> </tr> <tr> <td>Z5U</td> <td></td> <td>1±0.1kHz</td> <td>0.5±0.05Vrms</td> </tr> </tbody> </table>	Item	Char.	Frequency	Voltage	C0Δ to U2J, SL (1000pF and below)		1±0.1MHz	0.5 to 5Vrms	C0Δ to U2J, SL (more than 1000pF)		1±0.1kHz	1±0.2Vrms	X5R, X7R, Y5V (10μF and below)		1±0.1kHz	1±0.2Vrms	X5R, X7R, Y5V (more than 10μF)		120±24Hz	0.5±0.1Vrms	Z5U		1±0.1kHz	0.5±0.05Vrms
			Item		Char.	Frequency	Voltage																					
C0Δ to U2J, SL (1000pF and below)		1±0.1MHz	0.5 to 5Vrms																									
C0Δ to U2J, SL (more than 1000pF)		1±0.1kHz	1±0.2Vrms																									
X5R, X7R, Y5V (10μF and below)		1±0.1kHz	1±0.2Vrms																									
X5R, X7R, Y5V (more than 10μF)		120±24Hz	0.5±0.1Vrms																									
Z5U		1±0.1kHz	0.5±0.05Vrms																									
[Z5U] W.V. : 25Vmin. : 0.025max. [Y5V] W.V. : 25Vmin. : 0.05max.(C<10μF) : 0.09max.(C≥1.0μF) W.V. : 16V : 0.07max.(C<1.0μF) : 0.09max.(C≥1.0μF) W.V. : 10Vmax. : 0.125max.																												
9	Capacitance Change	Within the specified tolerance. (Table A)	X5R : Within ±15% (-55 to +85°C) X7R : Within ±15% (-55 to +125°C) Z5U : Within +22/-56% (+10 to +85°C) Y5V : Within +22/-82% (-30 to +85°C)	<p>The capacitance change shall be measured after 5 Min. at each specified temperature stage.</p> <p>(1) Temperature Compensating Type</p> <p>The temperature coefficient is determined using the Capacitance measured in step 3 as a reference.</p> <p>When cycling the temperature sequentially from step 1 through 5 (C0Δ : +25°C to +125°C : other temp. coeffs. : +25°C to +85°C) the capacitance shall be within the specified tolerance for the temperature coefficient and capacitance change as Table A.</p> <p>The capacitance drift is calculated by dividing the differences between the maximum and minimum measured values in the step 1,3 and 5 by the cap value in step 3.</p> <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature(°C)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>25±2</td> </tr> <tr> <td>2</td> <td>-55±3 (for CΔ to U2J/SL/X5R/X7R) -30±3 (for Y5V) 10±3 (for Z5U)</td> </tr> <tr> <td>3</td> <td>25±2</td> </tr> <tr> <td>4</td> <td>125±3 (for CΔ/X7R) 85±3 (for other TC)</td> </tr> <tr> <td>5</td> <td>25±2</td> </tr> </tbody> </table> <p>(2) High Dielectric Constant Type</p> <p>The ranges of capacitance change compared with the above 25°C value over the temperature ranges shown in the table shall be within the specified ranges.</p>	Step	Temperature(°C)	1	25±2	2	-55±3 (for CΔ to U2J/SL/X5R/X7R) -30±3 (for Y5V) 10±3 (for Z5U)	3	25±2	4	125±3 (for CΔ/X7R) 85±3 (for other TC)	5	25±2												
	Step	Temperature(°C)																										
	1	25±2																										
2	-55±3 (for CΔ to U2J/SL/X5R/X7R) -30±3 (for Y5V) 10±3 (for Z5U)																											
3	25±2																											
4	125±3 (for CΔ/X7R) 85±3 (for other TC)																											
5	25±2																											
Capacitance Temperature Characteristics	Temperature Coefficient	Within the specified tolerance. (Table A)	—																									
Capacitance Drift	Capacitance Drift	Within ±0.2% or ±0.05pF (Whichever is larger.) *Not apply to SL/25V	—																									

No.	Item	Specification		Test Method																																				
		Temperature Compensating Type	High Dielectric Type																																					
10	Adhesive Strength of Termination	No removal of the terminations or other defect shall occur.		<p>Solder the capacitor to the test jig (glass epoxy board) shown in Fig.1 using a eutectic solder. Then apply 10N* force in parallel with the test jig for 10±1sec. The soldering shall be done either with an iron or using the reflow method and shall be conducted with care so that the soldering is uniform and free of defects such as heat shock. *2N (GRM33) 5N (GRM36,GRM39)</p>  <p>Fig.1</p> <table border="1"> <thead> <tr> <th>Type</th> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr><td>GRM33</td><td>0.3</td><td>0.9</td><td>0.3</td></tr> <tr><td>GRM36</td><td>0.4</td><td>1.5</td><td>0.5</td></tr> <tr><td>GRM39</td><td>1.0</td><td>3.0</td><td>1.2</td></tr> <tr><td>GRM40</td><td>1.2</td><td>4.0</td><td>1.65</td></tr> <tr><td>GRM42-6</td><td>2.2</td><td>5.0</td><td>2.0</td></tr> <tr><td>GRM42-2</td><td>2.2</td><td>5.0</td><td>2.9</td></tr> <tr><td>GRM43-2</td><td>3.5</td><td>7.0</td><td>3.7</td></tr> <tr><td>GRM44-1</td><td>4.5</td><td>8.0</td><td>5.6</td></tr> </tbody> </table> <p style="text-align: right;">(in mm)</p>	Type	a	b	c	GRM33	0.3	0.9	0.3	GRM36	0.4	1.5	0.5	GRM39	1.0	3.0	1.2	GRM40	1.2	4.0	1.65	GRM42-6	2.2	5.0	2.0	GRM42-2	2.2	5.0	2.9	GRM43-2	3.5	7.0	3.7	GRM44-1	4.5	8.0	5.6
		Type	a		b	c																																		
GRM33	0.3	0.9	0.3																																					
GRM36	0.4	1.5	0.5																																					
GRM39	1.0	3.0	1.2																																					
GRM40	1.2	4.0	1.65																																					
GRM42-6	2.2	5.0	2.0																																					
GRM42-2	2.2	5.0	2.9																																					
GRM43-2	3.5	7.0	3.7																																					
GRM44-1	4.5	8.0	5.6																																					
11	Vibration Resistance	Appearance	No defects or abnormalities.	<p>Solder the capacitor to the test jig (glass epoxy board) in the same manner and under the same conditions as (10). The capacitor shall be subjected to a simple harmonic motion having a total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55Hz. The frequency range, from 10 to 55Hz and return to 10Hz, shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).</p>																																				
		Capacity	Within the specified tolerance.																																					
12	Deflection	Q/D.F.	<p>30pFmin. : Q_z≥1000 30pFmax. : Q_z≥400+20C C : Nominal Capacitance (pF)</p> <p>[X5R,X7R] W.V. : 25Vmin. : 0.025max. W.V. : 16/10V : 0.035max. W.V. : 6.3V : 0.05max. (C<3.3μF) 0.1max. (C≥3.3μF)</p> <p>[Z5U] W.V. : 25Vmin. : 0.025max.</p> <p>[Y5V] W.V. : 25Vmin. : 0.05max. (C<1.0μF) : 0.09max. (C≥1.0μF)</p> <p>W.V. : 16V : 0.07max. (C<1.0μF) : 0.09max. (C≥1.0μF)</p> <p>W.V. : 10Vmax.:0.125max.</p>	<p>Solder the capacitor on the test jig (glass epoxy board) shown in Fig.2 using a eutectic solder. Then apply a force in the direction shown in Fig. 3. The soldering shall be done either with an iron or using the reflow method and shall be conducted with care so that the soldering is uniform and free of defects such as heat shock.</p>  <p>Fig.2</p> <p>t : 1.6mm (GRM33/36 : 0.8mm)</p> <table border="1"> <thead> <tr> <th>Type</th> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr><td>GRM33</td><td>0.3</td><td>0.9</td><td>0.3</td></tr> <tr><td>GRM36</td><td>0.4</td><td>1.5</td><td>0.5</td></tr> <tr><td>GRM39</td><td>1.0</td><td>3.0</td><td>1.2</td></tr> <tr><td>GRM40</td><td>1.2</td><td>4.0</td><td>1.65</td></tr> <tr><td>GRM42-6</td><td>2.2</td><td>5.0</td><td>2.0</td></tr> <tr><td>GRM42-2</td><td>2.2</td><td>5.0</td><td>2.9</td></tr> <tr><td>GRM43-2</td><td>3.5</td><td>7.0</td><td>3.7</td></tr> <tr><td>GRM44-1</td><td>4.5</td><td>8.0</td><td>5.6</td></tr> </tbody> </table> <p style="text-align: right;">(in mm)</p>	Type	a	b	c	GRM33	0.3	0.9	0.3	GRM36	0.4	1.5	0.5	GRM39	1.0	3.0	1.2	GRM40	1.2	4.0	1.65	GRM42-6	2.2	5.0	2.0	GRM42-2	2.2	5.0	2.9	GRM43-2	3.5	7.0	3.7	GRM44-1	4.5	8.0	5.6
		Type	a		b	c																																		
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GRM44-1	4.5	8.0	5.6																																					
		No crack or marked defect shall occur.	 <p>Fig.3</p>																																					

No.	Item	Specification		Test Method															
		Temperature Compensating Type	High Dielectric Type																
13	Solderability of Termination	75% of the terminations is to be soldered evenly and continuously.		Immerse the capacitor in a solution of ethanol (JIS-K-8101) and rosin (JIS-K-5902) (25% rosin in weight proportion). Preheat at 80 to 120°C for 10 to 30 seconds. After preheating, immerse in eutectic solder solution for 2±0.5 seconds at 230±5°C.															
14	Resistance to Soldering Heat	The measured and observed characteristics shall satisfy the specifications in the following table.		<p>Preheat the capacitor at 120 to 150°C for 1 minute.</p> <p>Immerse the capacitor in a eutectic solder solution at 270±5°C for 10±0.5 seconds. Let sit at room temperature for 24±2 hours (temperature compensating type) or 48±4 hours (high dielectric constant type), then measure.</p> <p>•Initial measurement for high dielectric constant type Perform a heat treatment at 150±5°C for one hour and then let sit for 48±4 hours at room temperature. Perform the initial measurement.</p> <p>*Preheating for GRM42-2/43-2/44-1</p> <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature</th> <th>Time</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>100°C to 120°C</td> <td>1 min.</td> </tr> <tr> <td>2</td> <td>170°C to 200°C</td> <td>1 min.</td> </tr> </tbody> </table>	Step	Temperature	Time	1	100°C to 120°C	1 min.	2	170°C to 200°C	1 min.						
		Step	Temperature		Time														
		1	100°C to 120°C		1 min.														
		2	170°C to 200°C		1 min.														
		Appearance	No marking defects.																
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)		X5R,X7R : Within ±7.5% Z5U,Y5V : Within ±20%														
Q/D.F.	30pFmin. : Q≥1000 30pFmax. : Q≥400+20C C:Nominal Capacitance (pF)	[X5R,X7R] W.V. : 25Vmin. : 0.025max. W.V. : 16/10V : 0.035max. W.V. : 6.3V : 0.05max. (C<3.3μF) 0.1max. (C≥3.3μF) [Z5U] W.V. : 25Vmin. : 0.025max. [Y5V] W.V. : 25Vmin. : 0.05max. (C<1.0μF) : 0.09max. (C≥1.0μF) W.V.:16V : 0.07max. (C<1.0μF) : 0.09max. (C≥1.0μF) W.V. : 10Vmax. : 0.125max.																	
I.R.	More than 10,000MΩ or 500Ω•F (Whichever is smaller)																		
Dielectric Strength	No failure																		
15	Temperature Cycle	The measured and observed characteristics shall satisfy the specifications in the following table.		<p>Fix the capacitor to the supporting jig in the same manner and under the same conditions as (10). Perform the five cycles according to the four heat treatments listed in the following table. Let sit for 24±2 hours (temperature compensating type) or 48±4 hour (high dielectric constant type) at room temperature, then measure.</p> <table border="1"> <thead> <tr> <th>Step</th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> </tr> </thead> <tbody> <tr> <td>Temp. (°C)</td> <td>Min. Operating Temp.+0/-3</td> <td>Room Temp.</td> <td>Max. Operating Temp.+3/-0</td> <td>Room Temp.</td> </tr> <tr> <td>Time(min.)</td> <td>30±3</td> <td>2 to 3</td> <td>30±3</td> <td>2 to 3</td> </tr> </tbody> </table> <p>•Initial measurement for high dielectric constant type Perform a heat treatment at 150±5°C for one hour and then let sit for 48±4 hours at room temperature. Perform the initial measurement.</p>	Step	1	2	3	4	Temp. (°C)	Min. Operating Temp.+0/-3	Room Temp.	Max. Operating Temp.+3/-0	Room Temp.	Time(min.)	30±3	2 to 3	30±3	2 to 3
		Step	1		2	3	4												
		Temp. (°C)	Min. Operating Temp.+0/-3		Room Temp.	Max. Operating Temp.+3/-0	Room Temp.												
		Time(min.)	30±3		2 to 3	30±3	2 to 3												
		Appearance	No marking defects.																
		Capacitance Change	Within ±2.5% or ±0.25pF (Whichever is larger)		X5R,X7R : Within ±7.5% Z5U,Y5V : Within ±20%														
Q/D.F.	30pFmin. : Q≥1000 30pFmax. : Q≥400+20C C : Nominal Capacitance (pF)	[X5R,X7R] W.V. : 25Vmin. : 0.025max. W.V. : 16/10V:0.035max. W.V. : 6.3V 0.05max. (C<3.3μF) 0.1max. (C≥3.3μF) [Z5U] W.V. : 2.5Vmin. : 0.025max. [Y5V] W.V. : 25Vmin. : 0.05max. (C<1.0μF) : 0.09max. (C≥1.0μF) W.V. : 16V : 0.07max. (C<1.0μF) : 0.09max. (C≥1.0μF) W.V. : 10Vmax. : 0.125max.																	
I.R.	More than 10,000MΩ or 500Ω•F (Whichever is smaller)																		
Dielectric Strength	No failure																		

Continued from the preceding page.

No.	Item	Specification		Test Method	
		Temperature Compensating Type	High Dielectric Type		
16	Humidity Steady State	The measured and observed characteristics shall satisfy the specifications in the following table.			Sit the capacitor at 40±2°C and 90 to 95% humidity for 500±12 hours. Remove and let sit for 24±2 hours (temperature compensating type) or 48±4 hours (high dielectric constant type) at room temperature, then measure.
		Appearance	No marking defects.		
		Capacitance Change	Within ±5% or ±0.5pF (Whichever is larger)	X5R,X7R : Within ±12.5% Z5U,Y5V : Within ±30%	
		Q/D.F.	30pF and over: $Q \geq 350$ 10pF and over 30pF and below : $Q \geq 275 + 5C/2$ 10pF and below : $Q \geq 200 + 10C$ C : Nominal Capacitance (pF)	[X5R,X7R] W.V. : 25Vmin. : 0.05max. W.V. : 16/10V : 0.05max. W.V. : 6.3V 0.075max. (C<3.3μF) 0.125max. (C≥3.3μF) [Z5U] W.V. : 25Vmin.:0.05max. [Y5V] W.V. : 25Vmin. : 0.075max. (C<1.0μF) : 0.0125max.(C≥1.0μF) W.V. : 16V : 0.1max. (C<1.0μF) : 0.125max. (C≥1.0μF) W.V. : 10Vmax. : 0.15max.	
		I.R.	More than 1,000MΩ or 50Ω·F(Whichever is smaller)		
		Dielectric Strength	No failure		
17	Humidity Load	The measured and observed characteristics shall satisfy the specifications in the following table.			Apply the rated voltage at 40±2°C and 90 to 95% humidity for 500±12 hours. Remove and let sit for 24±2 hours (temperature compensating type) or 48±4 Hours (high dielectric constant type) at room temperature, then measure. The charge/discharge current is less than 50mA. •Initial measurement for Y5V/10Vmax. Apply the rated DC voltage for 1 hour at 40±2°C. Remove and let sit for 48±4 hours at room temperature. Perform initial measurement.
		Appearance	No marking defects.		
		Capacitance Change	Within ±7.5% or ±0.75pF (Whichever is larger)	X5R,X7R : Within ±12.5% Z5U : Within ±30% Y5V : Within ±30% [W.V. : 10Vmax.] Y5V : Within +30/-40%	
		Q/D.F.	30pF and over : $Q \geq 200$ 30pF and below : $Q \geq 100 \pm 10C/3$ C : Nominal Capacitance (pF)	[X5R,X7R] W.V. : 25Vmin. : 0.05max. W.V. : 16/10V : 0.05max. W.V. : 6.3V 0.075max. (C<3.3μF) 0.125max. (C≥3.3μF) [Z5U] W.V. : 25Vmin. : 0.05max. [Y5V] W.V. : 25Vmin. : 0.075max. (C<10μF) : 0.0125max. (C≥1.0μF) W.V. : 16V : 0.1max. (C<1.0μF) : 0.125max. (C≥1.0μF) W.V. : 10Vmax. : 0.15max.	
		I.R.	More than 500MΩ or 25Ω·F(Whichever is smaller)		
		Dielectric Strength	No failure		

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No.	Item	Specification		Test Method
		Temperature Compensating Type	High Dielectric Type	
18		The measured and observed characteristics shall satisfy the specifications in the following table.		<p>Apply 200% of the rated voltage for 1000±12 hours at the maximum operating temperature ±3°C. Let sit for 24±2 hours (temperature compensating type) or 48±4 hours (high dielectric constant type) at room temperature, then measure.</p> <p>The charge/discharge current is less than 50mA.</p> <p>•Initial measurement for high dielectric constant type.</p> <p>Apply 200% of the rated DC voltage for one hour at the maximum operating temperature ±3°C. Remove and let sit for 48±4 hours at room temperature. Perform initial measurement.</p> <p>*150% for 500V</p>
	Appearance	No marking defects.		
	Capacitance Change	Within ±3% or ±0.3pF (Whichever is larger)	X5R,X7R : Within ±12.5% Z5U : Within ±30% Y5V : Within ±30% (Cap<1.0μF) Y5V : Within +30/-40%(Cap≥1.0μF)	
	High Temperature Load			
	Q/D.F.	30pF and over : $Q \geq 350$ 10pF and over 30pF and below : $Q \geq 275 \pm 5C/2$ 10pF and below : $Q \geq 200 \pm 10C$ C : Nominal Capacitance (pF)	[X5R,X7R] W.V. : 25Vmin. : 0.05max. W.V. : 16/10V : 0.05max. W.V. : 6.3V : 0.075max. (C<3.3μF) : 0.125max. (C≥3.3μF) [Z5U] W.V. : 25Vmin. : 0.05max [Y5V] W.V. : 25Vmin. : 0.075max. (C<1.0μF) : 0.0125max.(C≥1.0μF) W.V. : 16V : 0.1max. (C<1.0μF) : 0.125max. (C≥1.0μF) W.V. : 10Vmax. : 0.15max.	
	I.R.	More than 1,000MΩ or 50Ω•F(Whichever is smaller)		
Dielectric Strength	No failure			
19	Notice	When mounting capacitor of 500V rated voltage, perform the epoxy resin coating(min.1.0mm thickness)		

Table A

Char.	Nominal Values (ppm/°C)*	Capacitance Change from 25°C (%)					
		-55		-30		-10	
		Max.	Min.	Max.	Min.	Max.	Min.
C0G	0± 30	0.58	-0.24	0.40	-0.17	0.25	-0.11
C0H	0± 60	0.87	-0.48	0.59	-0.33	0.38	-0.21
P2H	-150± 60	2.33	0.72	1.61	0.50	1.02	0.32
R2H	-220± 60	3.02	1.28	2.08	0.88	1.32	0.56
S2H	-330± 60	4.09	2.16	2.81	1.49	1.79	0.95
T2H	-470± 60	5.46	3.28	3.75	2.26	2.39	1.44
U2J	-750±120	8.78	5.04	6.04	3.47	3.84	2.21
SL	+350 to -1000	-	-	-	-	-	-

*Nominal values denote the temperature coefficient within a range of 25°C to 125°C (for CoΔ)/85°C (for other TC).