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FAIRCHILD

SEMICONDUCTOR

DM74ALS02 Quad 2-Input NOR Gate

General Description

This device contains four independent gates, each of which performs the logic NOR function.

September 1986 Revised February 2000

Features

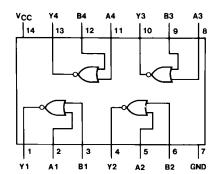
- Switching specifications at 50 pF
- \blacksquare Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky and low power Schottky TTL counterpart
- Improved AC performance over Schottky and low power Schottky counterparts

Ordering Code:

Order Number	Package Number	Package Description		
DM74ALS02M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow		
DM74ALS02SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide		
DM74ALS02N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide		
Devices also available in Tane and Reel. Specify by appending the suffix letter "X" to the ordering code				

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering of

Connection Diagram



Function Table

 $\mathbf{Y} = \mathbf{A} + \mathbf{B}$

	Inputs	Output	ıt	
Α	В	Y		
L	L	Н		
L	Н	L		
н	L	L		
н	н	L		

H = HIGH Logic Level L = LOW Logic Level

Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^{\circ}C$ to $+150^{\circ}C$
Typical θ_{JA}	
N Package	86.5°C/W
M Package	116.0°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.5	5	5.5	V
VIH	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
он	HIGH Level Output Current			-0.4	mA
lol	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

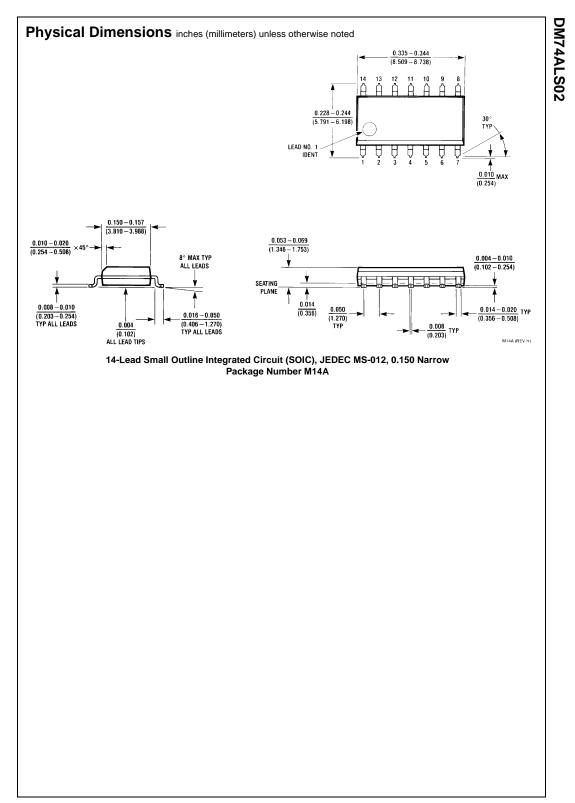
over recommended operating free air temperature range. All typical values are measured at V_{CC} = 5V, $T_A = 25^{\circ}C$.

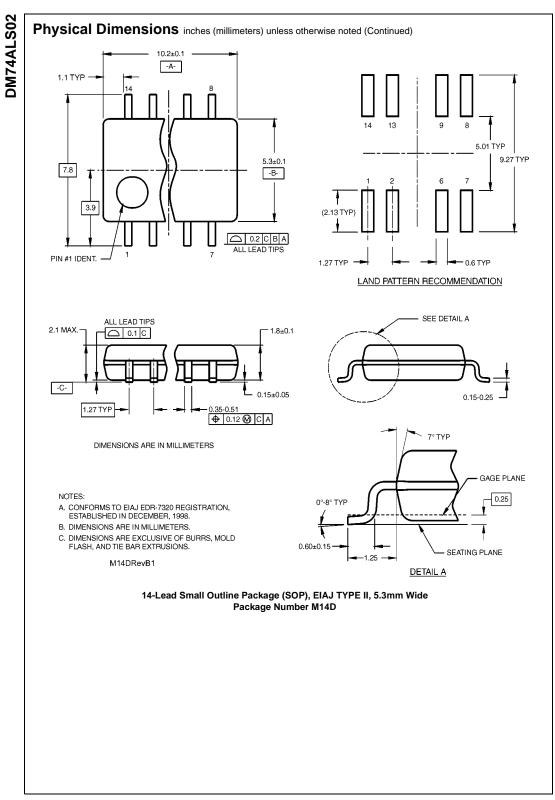
Symbol	Parameter	Condition	Conditions		Тур	Max	Units
V _{IK}	Input Clamp Voltage	$V_{CC} = 4.5 V$, $I_I = -18 \text{ mA}$	$V_{CC} = 4.5 V, I_{I} = -18 \text{ mA}$			-1.5	V
V _{OH}	HIGH Level	I _{OH} = -0.4 mA		V _{CC} - 2			V
	Output Voltage	$V_{CC} = 4.5V$ to $5.5V$	$V_{CC} = 4.5V$ to 5.5V				v
V _{OL}	LOW Level	V _{CC} = 4.5V	I _{OL} = 8 mA		0.35	0.5	V
C	Output Voltage		10L - 0 IIIX		0.55	0.5	v
l _l	Input Current @ Max.	V _{CC} = 5.5V, V _{IH} = 7V	•			0.1	mA
	Input Voltage	v _{CC} = 5.5v, v _{IH} = 7v				0.1	- IIIA
IIH	HIGH Level Input Current	$V_{CC} = 5.5 V, V_{IH} = 2.7 V$				20	μA
IIL	LOW Level Input Current	$V_{CC} = 5.5V, V_{IL} = 0.4V$				-0.1	mA
I _O	Output Drive Current	$V_{CC} = 5.5V$	$V_0 = 2.25V$	-30		-112	mA
I _{CC}	Supply Current	$V_{CC} = 5.5V$	Outputs HIGH		0.85	2.2	mA
			Outputs LOW		2.16	4	mA

Switching Characteristics

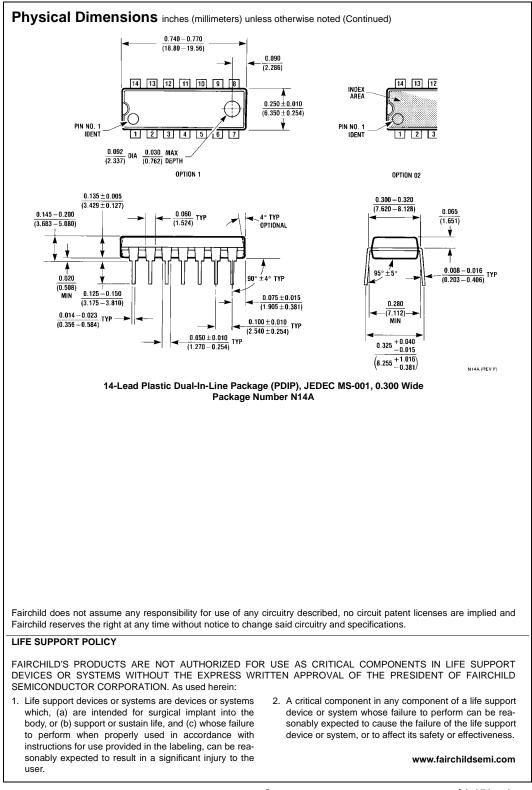
Symbol	Parameter	Conditions	Min	Max	Units
t _{PLH}	Propagation Delay Time	$V_{CC} = 4.5V$ to 5.5V	3	12	ns
	LOW-to-HIGH Level Output	$R_L = 500\Omega$			
t _{PHL}	Propagation Delay Time	$C_L = 50 \text{ pF}$	2	10	ns
	HIGH-to-LOW Level Output		3		

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DM74ALS02 Quad 2-Input NOR Gate

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