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Please read this notice before using the TAIYO YUDEN products.

REMINDERS

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- Please contact Taiyo Yuden Co., Ltd. for further details of product specifications as the individual specification is available.
- Please conduct validation and verification of products in actual condition of mounting and operating environment before commercial shipment of the equipment.
- All electronic components or functional modules listed in this catalog are developed, designed and intended for use in general electronics equipment.(for AV, office automation, household, office supply, information service, telecommunications, (such as mobile phone or PC) etc.). Before incorporating the components or devices into any equipment in the field such as transportation,(automotive control, train control, ship control), transportation signal, disaster prevention, medical, public information network (telephone exchange, base station) etc. which may have direct influence to harm or injure a human body, please contact Taiyo Yuden Co., Ltd. for more detail in advance. Do not incorporate the products into any equipment in fields such as aerospace, aviation, nuclear control, submarine system, military, etc. where higher safety and reliability are especially required.

In addition, even electronic components or functional modules that are used for the general electronic equipment, if the equipment or the electric circuit require high safety or reliability function or performances, a sufficient reliability evaluation check for safety shall be performed before commercial shipment and moreover, due consideration to install a protective circuit is strongly recommended at customer's design stage.

- The contents of this catalog are applicable to the products which are purchased from our sales offices or distributors (so called "TAIYO YUDEN's official sales channel").

 It is only applicable to the products purchased from any of TAIYO YUDEN's official sales channel.
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HIGH VALUE MULTILAYER CERAMIC CAPACITORS





VAVE REFLOW

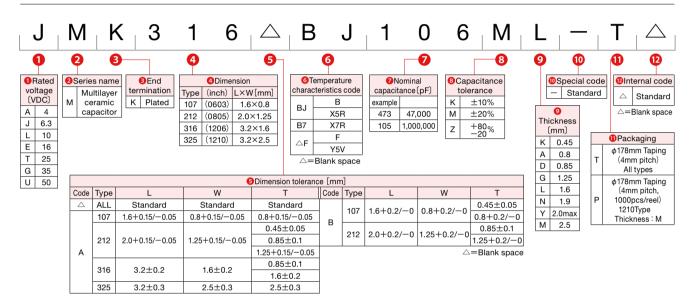
■ FEATURES

- The use of nickel as electrode material and plating processing improve the solderability and heat resistance characteristics. It also prevents migration and raises the level of reliability.
- Low equivalent series resistance(ESR) provides superior noise absorption characteristics.
- Compared to tantalum or aluminum electrolytic capacitors, multilayer ceramic capacitors offer a number of superior features, including: Higher permissible ripple current values
 Smaller case sizes with high rated voltage Improved reliability due to higher insulation resistance and breakdown voltage.

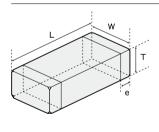
APPLICATIONS

- General digital circuit
- Power supply bypass capacitors
 Liquid crystal modules
 Liquid crystal drive voltage lines
 LSI, IC, converters(both for input and output)
- Smoothing capacitors
 DC-DC converters (for both input and output)
 Switching power supplies (secondary side)

PART NUMBER



STANDARD EXTERNAL DIMENSIONS/STANDARD QUANTITY



Туре			Dimension [mm]			Standard	quantity [pcs]
туре	L	W	Т		е	Paper tape	Embossed tape
□MK107	1.6±0.10	0.8±0.10	0.45±0.05	K	0.35±0.25	4000	
(0603 inch)	1.0±0.10	0.6±0.10	0.8±0.10	Α	0.33±0.23	4000	_
			0.45±0.05	K		4000	
☐MK212 (0805 inch)	2.0±0.10	1.25±0.10	0.85±0.10	D	0.5±0.25	4000	_
(0003 111011)			1.25±0.10	G		_	3000
			0.85±0.10	D		4000	_
☐MK316 (1206 inch)	3.2±0.15	1.6±0.15	1.25±0.10	G	0.5+0.35/-0.25		3000
(1200 IIICII)			1.6±0.20	L		_	2000
			0.85±0.10	D			
☐MK325	3.2±0.30	2.5±0.20	1.9±0.20	N	0.6±0.3		2000
(1210 inch)	3.2±0.30	2.5±0.20	1.9+0.1/-0.2	Υ	U.6±U.3	_	
			2.5±0.20	М			500(T), 1000(P)

■ AVAILABLE CAPACITANCE RANGE

	Tuno							1	07													2	12												3	316													32	5					
Cap	Туре			7R					3/X					F/Y					(7R					X5F				/5V			X7					3/X					Y5\				K7F					3/X				-/Y	
(μF)		50	25	16 1	0 6	.3 5	0 3	5 25	5 16	3 1	0 6.	3 4	50	25	16	10	50	35 2	5 16	10	6.3	50	25	16 1	0 6.3	3 50	16	10	6.3	50 2	5 16	10	6.3	50	25	16	10 6	i.3 4	1 3	5 25	5 16	10	50	25	16	10	6.3	50	35 2	25 1	16 1	0 6.3	16	3 10	6.
	(3-digit)					Т																							\Box										Т	\perp															
0.1	104					┸							Α		Ш		G					G																					Ш												L
0.15	154																																																						L
0.22	224	Α		A	A /	4	Т	Α	A	\ A	١ -	Т	Т		Α		G		Т			G		Т	Т	Т	Т		П	L	Т	П	П			П		Т	Т	Т	Т	П	П				П	П		П		Т	Т	Т	Г
0.33	334			\neg	T	Т	Т	Т	Т	Т	Т	Т	Т		П		П		Т				Т	Т		Т				Т		П					Т	Т	Т	Т	Т	Т	П				П	П	П	П		Т	Т	Т	Г
0.47	474	Α	Α	A	4 /	4 /	4	Α	A	A	1		Т	Α	Α		G		Т			G	T	T	\top	G	ì		T	L	\top		П		T	T	T	Т	Т	Т	Т		П				П	\neg		T		Т	Т	Т	Т
0.68	684	П		\neg	T	Т		Т	Т	Т	Т		Т	П	П				Т				T	T	\top	Т	T		T	T			П				T	Т	Т	Т	Т		П				П	Т	T	T		Т	Т	Т	Г
1	105		Α	A	A /	4	A	A	A	A	1	T	Т		Α	Α	G	G	a G	G		G	T	\top	\top	G	ì		寸	LI			П	L		\neg	\top	\top	Т	\top	\top	T	П				П	\top		\dashv	\top	\top	Т	\top	Т
2.2	225			1	4	T		Α	A	A	A A				Α	Α		(a G	G			G	G	3	T	G		一	LI	L	L	Πİ		L	L				\top			П				T	7	N	\neg		\top		\top	Т
3.3	335					T									П														T	1			П			T				T			П	N				T		N				T	Т
4.7	475					T		ı	Α	A	A	1			П			(G G	G			G	G	G G	ì		G	T	LI		L		L	L	L		1					М	N	N			М	N	N	N		T		Т
6.8	685					T		T	Т										Ť					1		T			T									T		T			П					T					T	T	Т
10	106			T	T	T		T	T	A	A	A			П		T		T	G	G		-	G	G G	à		G	G	I	L	L	L		L	L	LI	L	L	L	L		П		N	N		М	N	A.N	NN	1	T	T	Т
22	226				T	T		T	T	Т		Α			П		П		T					(G G	ì		П	寸	T		L	Πİ			L	LI		T	T		L	П	М	М		T	┑		- 1	ММ	.Y Y	N	IN	Т
47	476	П	\neg	\top	\top	\top	Т	Т	T	T	Т		Т	П	П	\neg	\Box	\top	Т	Т			T	T	G	à	Т		ヿ	\top	Т	Т	П				LI	L	Т	\top	Т	Т	П			М	М	\top	T		MIN	/ M.N	V	\top	N
100	107	П		\top	\top	\top	\top	\top	T	T	\top	T	Т					\top					\neg	\top		Т	Т		\neg	\top			П					LL		\top	\top		П				П	\top		7	N	/ M.	1	\top	Т
	Letters	s in	the	tab	ole	ind	ica	te p	ro	du	ct t	hick	kne	SS.			X5F	R on	ly													_										_				_		_				نسندا کید			_

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Low Profile Multilayer Ceramic Capacitors

	_		10	07				_		- ;	212									316	;					325	5
Cap	Type		B/>	(5F		Χī	7R		_	3/X	5R		F.	/Y5	V		В	/X5	R			F/Y	′5V		В	/X5	R
(μF)	VDC	25	16	10	6.3	16	10	50	25	16	10	6.3	50	10	6.3	50	25	16	10	6.3	50	35	10	6.3	25	16	10
	(3-digit)																										
0.1	104																										
0.22	224												D														
0.33	334																										
0.47	474					D			D																		
0.68	684																										
1	105	K	K	K	Κ	D	D	D	D	D	D					D	D										
2.2	225			K	K	D	D		D	D	D			D		D	D	D			G						
3.3	335																										D
4.7	475			K	K		D		D	D	D. K	D.K			D	D	D	D	D			G	D				D
6.8	685																										
10	106									D	D	D.K					D	D	D	D				D	D	D	D
22	226											D						D	D	D						D	
47	476																			D							

Tama abau		Temp	erature char	acteristics		Consoltance
Temp.char. Code	Appli stan	cable dard	Temperature range(°C)	Ref. Temp.	Capacitance change (%)	Capacitance tolerance(%)
BJ	JIS	В	-25~+85	20	±10	1.40(14)
БJ	EIA	X5R	-55~+85	25	±15	±10(K) ±20(M)
B7	EIA	X7R	-55~+125	25	±15	ZO(IVI)
F	JIS	F	-25~+85	20	+30/-80	+80 -20 ^(Z)
г	EIA	Y5V	-30~+85	25	+22/-82	-20 ^(Z)

Note: Letters in the table indicate product thickness. X5R only

■ REPRESENTATIVE PART NUMBERS

●107TYPE

[Temperature Characteristic BJ:B/X5R]

· 0.8mm thickness(A)

Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance [µF]	Capacitance tolerance (%)	tanδ [%]	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
50V	UMK107 BJ474□A	UMK107ABJ474□A	X5R	0.47	±10, ±20	10	0.8+0.15/-0.05	R	150%	D	
	UMK107 BJ105□A		X5R	1	±10, ±20	10	0.8±0.1	R	150%		
35V	GMK107 BJ105□A		B/X5R	1	±10, ±20	5	0.8±0.1	R	150%		
25V	TMK107 BJ224□A		B/X5R	0.22	±10, ±20	3.5	0.8±0.1	R/W	200%		
	TMK107 BJ474□A		B/X5R	0.47	±10, ±20	3.5	0.8±0.1	R	150%		
	TMK107 BJ105□A		B/X5R	1	±10, ±20	5	0.8±0.1	R	150%		
	TMK107 BJ225□A	TMK107ABJ225□A	X5R	2.2	±10, ±20	10	0.8+0.15/-0.05	R	150%	D	
16V	EMK107 BJ224□A		B/X5R*1	0.22	±10, ±20	3.5	0.8±0.1	R/W	200%		
	EMK107 BJ474□A		B/X5R*1	0.47	±10, ±20	3.5	0.8±0.1	R	200%		
	EMK107 BJ105□A		B/X5R*1	1	±10, ±20	5	0.8±0.1	R	150%		
	EMK107 BJ225□A		B/X5R	2.2	±10, ±20	10	0.8±0.1	R	150%		
	EMK107 BJ475□A	EMK107ABJ475□A	X5R	4.7	±10, ±20	10	0.8+0.15/-0.05	R	150%	D	
10V	LMK107 BJ224□A		B/X5R*1	0.22	±10, ±20	3.5	0.8±0.1	R/W	200%		
	LMK107 BJ474□A		B/X5R*1	0.47	±10, ±20	3.5	0.8±0.1	R	200%		
	LMK107 BJ105□A		B/X5R*1	1	±10, ±20	5	0.8±0.1	R	200%		
	LMK107 BJ225□A		B/X5R	2.2	±10, ±20	10	0.8±0.1	R	150%		
	LMK107 BJ475□A		X5R	4.7	±10, ±20	10	0.8±0.1	R	150%		
	LMK107 BJ106MA	LMK107BBJ106MA	X5R	10	±20	10	0.8+0.2/-0	R	150%	D	Special code: L
6.3V	JMK107 BJ225□A		B/X5R	2.2	±10, ±20	10	0.8±0.1	R	150%		
	JMK107 BJ475□A		X5R	4.7	±10, ±20	10	0.8±0.1	R	150%		
	JMK107 BJ106MA	JMK107ABJ106MA	X5R	10	±20	10	0.8+0.15/-0.05	R	150%		
4V	AMK107 BJ106MA		X5R	10	±20	10	0.8±0.1	R	150%		
	AMK107 BJ226MA	AMK107BBJ226MA	X5R	22	±20	10	0.8+0.2/-0	R	150%		

· 0.45mm thickness(K)

Rated			T	Canasitanas	Capacitance	Z 4	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	Temp. char.	Capacitance (µF)	tolerance (%)	tanδ (%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
25V	TMK107 BJ105□K		X5R	1	±10, ±20	10	0.45±0.05	R	150%		
16V	EMK107 BJ105□K		X5R	1	±10, ±20	10	0.45±0.05	R	150%		
10V	LMK107 BJ105□K		B/X5R	1	±10, ±20	10	0.45±0.05	R	150%		
	LMK107 BJ225□K		X5R	2.2	±10, ±20	10	0.45±0.05	R	150%		
	LMK107 BJ475MK	LMK107BBJ475MK	X5R	4.7	±20	10	0.45±0.05	R	150%	D	Special code : L
6.3V	JMK107 BJ105□K		B/X5R	1	±10, ±20	10	0.45±0.05	R	150%		
	JMK107 BJ225□K		X5R	2.2	±10, ±20	10	0.45±0.05	R	150%		
	JMK107 BJ475MK		X5R	4.7	±20	10	0.45±0.05	R	150%		

Capacitance tolerance code is applied to $\hfill \square$ of part number.

[Temperature Characteristic B7:X7R]

Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(μF)	tolerance (%)	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
50V	UMK107 B7224□A		X7R	0.22	±10, ±20	10	0.8±0.1	R	150%	R	
	UMK107 B7474□A		X7R	0.47	±10, ±20	10	0.8±0.1	R	150%	R	
25V	TMK107 B7474□A		X7R	0.47	±10, ±20	10	0.8±0.1	R	150%	R	
	TMK107 B7105□A		X7R	1	±10, ±20	10	0.8±0.1	R	150%		
16V	EMK107 B7224□A		X7R	0.22	±10, ±20	3.5	0.8±0.1	R/W	150%		
	EMK107 B7474□A		X7R	0.47	±10, ±20	3.5	0.8±0.1	R	150%		
	EMK107 B7105□A		X7R	1	±10, ±20	5	0.8±0.1	R	150%		
10V	LMK107 B7224□A		X7R	0.22	±10, ±20	3.5	0.8±0.1	R/W	200%		
	LMK107 B7474□A		X7R	0.47	±10, ±20	3.5	0.8±0.1	R	200%		
	LMK107 B7105□A		X7R	1	±10, ±20	5	0.8±0.1	R	150%		
	LMK107 B7225□A		X7R	2.2	±10, ±20	10	0.8±0.1	R	150%		
6.3V	JMK107 B7224□A		X7R	0.22	±10, ±20	3.5	0.8±0.1	R/W	200%		
	JMK107 B7474□A		X7R	0.47	±10, ±20	3.5	0.8±0.1	R	200%		
	JMK107 B7105□A		X7R	1	±10, ±20	5	0.8±0.1	R	150%		

Capacitance tolerance code is applied to $\hfill\square$ of part number.

^{*1} We may provide X7R for some itemes according to the individual specification.

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■ REPRESENTATIVE PART NUMBERS

[Temperature Characteristic F:Y5V]

Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(μF)	tolerance (%)	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
50V	UMK107 F104ZA		F/Y5V	0.1	+80/-20	7	0.8±0.1	R/W	200%		
25V	TMK107 F474ZA		F/Y5V	0.47	+80/-20	7	0.8±0.1	R/W	200%		
16V	EMK107 F224ZA		F/Y5V	0.22	+80/-20	7	0.8±0.1	R/W	200%		
	EMK107 F474ZA		F/Y5V	0.47	+80/-20	7	0.8±0.1	R/W	200%		
	EMK107 F105ZA		F/Y5V	1	+80/-20	16	0.8±0.1	R	200%		
	EMK107 F225ZA		F/Y5V	2.2	+80/-20	16	0.8±0.1	R	200%		
10V	LMK107 F105ZA		F/Y5V	1	+80/-20	16	0.8±0.1	R	200%		
	LMK107 F225ZA		F/Y5V	2.2	+80/-20	16	0.8±0.1	R	200%		

●212TYPE

[Temperature Characteristic BJ : B/X5R]

·1.25mm thickness(G)

			_		Capacitance			Soldering	HALT	Internal	
Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance (µF)	tolerance (%)	tanδ (%)	Thickness (mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
50V	UMK212 BJ104□G		B/X5R*1	0.1	±10, ±20	3.5	1.25±0.1	R/W	200%		
	UMK212 BJ224□G		B/X5R*1	0.22	±10, ±20	3.5	1.25±0.1	R/W	150%		
	UMK212 BJ474□G		B/X5R*1	0.47	±10, ±20	3.5	1.25±0.1	R/W	150%		
	UMK212 BJ105□G		B/X5R	1	±10, ±20	5	1.25±0.1	R/W	150%		
25V	TMK212 BJ225□G		B/X5R	2.2	±10, ±20	5	1.25±0.1	R	150%		
	TMK212 BJ475□G	TMK212ABJ475□G	X5R	4.7	±10, ±20	10	1.25+0.15/-0.05	R	150%		
16V	EMK212 BJ225□G		B/X5R*1	2.2	±10, ±20	5	1.25±0.1	R	200%		
	EMK212 BJ475□G	EMK212ABJ475□G	B/X5R*1	4.7	±10, ±20	5	1.25+0.15/-0.05	R	150%		
	EMK212 BJ106□G	EMK212ABJ106□G	X5R	10	±10, ±20	10	1.25+0.15/-0.05	R	150%		
10V	LMK212 BJ225□G		B/X5R*1	2.2	±10, ±20	5	1.25±0.1	R	200%		
	LMK212 BJ475□G	LMK212ABJ475□G	B/X5R*1	4.7	±10, ±20	5	1.25+0.15/-0.05	R	200%		
	LMK212 BJ106□G	LMK212ABJ106□G	X5R	10	±10, ±20	10	1.25+0.15/-0.05	R	200%		
	LMK212 BJ226MG	LMK212BBJ226MG	X5R	22	±20	10	1.25+0.2/-0	R	150%		
6.3V	JMK212 BJ475□G	JMK212ABJ475□G	B/X5R	4.7	±10, ±20	5	1.25+0.15/-0.05	R	200%		
	JMK212 BJ106□G	JMK212ABJ106□G	X5R*1	10	±10, ±20	10	1.25+0.15/-0.05	R	200%		
	JMK212 BJ226MG	JMK212ABJ226MG	X5R	22	±20	10	1.25+0.15/-0.05	R	150%		
	JMK212 BJ476MG	JMK212BBJ476MG	X5R	47	±20	10	1.25+0.2/-0	R	150%		

· 0.85mm thickness (D)

			_		Capacitance			Soldering	HALT	Internal	
Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance (µF)	tolerance (%)	tanδ (%)	Thickness (mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
50V	UMK212 BJ105□D	UMK212ABJ105□D	X5R	1	±10, ±20	10	0.85±0.1	R	150%	D	
25V	TMK212 BJ474□D		B/X5R	0.47	±10, ±20	3.5	0.85±0.1	R	200%		
	TMK212 BJ105□D		B/X5R	1	±10, ±20	5	0.85±0.1	R	200%		
	TMK212 BJ225□D	TMK212ABJ225□D	B/X5R	2.2	±10, ±20	5	0.85±0.1	R	150%		
	TMK212 BJ475□D	TMK212BBJ475□D	X5R	4.7	±10, ±20	10	0.85±0.1	R	150%	D	
16V	EMK212 BJ105□D		B/X5R*1	1	±10, ±20	5	0.85±0.1	R	200%		
	EMK212 BJ225□D	EMK212ABJ225□D	B/X5R*1	2.2	±10, ±20	5	0.85±0.1	R	200%		
	EMK212 BJ475□D		B/X5R	4.7	±10, ±20	10	0.85±0.1	R	150%		
	EMK212 BJ106□D	EMK212ABJ106□D	X5R	10	±10, ±20	10	0.85±0.1	R	150%	D	
10V	LMK212 BJ105□D		B/X5R*1	1	±10, ±20	3.5	0.85±0.1	R	200%		
	LMK212 BJ225□D		B/X5R*1	2.2	±10, ±20	5	0.85±0.1	R	200%		
	LMK212 BJ475□D		B/X5R	4.7	±10, ±20	10	0.85±0.1	R	200%		
	LMK212 BJ106□D	LMK212ABJ106□D	X5R	10	±10, ±20	10	0.85±0.1	R	150%		
6.3V	JMK212 BJ475□D		X5R	4.7	±10, ±20	10	0.85±0.1	R	200%		
	JMK212 BJ106□D	JMK212ABJ106□D	X5R	10	±10, ±20	10	0.85±0.1	R	200%		
	JMK212 BJ226MD	JMK212ABJ226MD	X5R	22	±20	10	0.85±0.1	R	150%		·

· 0.45mm thickness(K)

	,										
Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(μF)	tolerance	[%]	(mm)	R:Reflow	% Rated	code	Note
voltago			onan.	(μι)	(%)	(70)	(IIIII)	W:Wave	voltage	(P/N 1)	
10V	LMK212 BJ475□K	LMK212ABJ475□K	X5R	4.7	±10, ±20	10	0.45±0.05	R	150%		
6.3V	JMK212 BJ475□K	JMK212ABJ475□K	X5R	4.7	±10, ±20	10	0.45±0.05	R	150%		
	JMK212 BJ106MK	JMK212ABJ106MK	X5R	10	±20	10	0.45±0.05	R	150%		

Capacitance tolerance code is applied to ☐ of part number.

*1 We may provide X7R for some itemes according to the individual specification.

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[Temperature Characteristic B7 : X7R] •1.25mm thickness(G)

1.2011111	Inickness(G)										
Rated	Part number 1	Part number 2	Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering R:Reflow	HALT	Internal code	Note
voltage	Part number i	Part number 2	char.	(μF)	tolerance (%)	(%)	(mm)	W:Wave	% Rated voltage	(P/N 1)	Note
50V	UMK212 B7104□G		X7R	0.1	±10, ±20	3.5	1.25±0.1	R/W	200%		
	UMK212 B7224□G		X7R	0.22	±10, ±20	3.5	1.25±0.1	R/W	150%		
	UMK212 B7474□G		X7R	0.47	±10, ±20	3.5	1.25±0.1	R/W	150%		
	UMK212 B7105□G		X7R	1	±10, ±20	10	1.25±0.1	R/W	150%		
35V	GMK212 B7105□G		X7R	1	±10, ±20	3.5	1.25±0.1	R/W	150%		
25V	TMK212 B7105□G		X7R	1	±10, ±20	3.5	1.25±0.1	R/W	150%		
	TMK212 B7225□G		X7R	2.2	±10, ±20	10	1.25±0.1	R	150%	R	
	TMK212 B7475□G	TMK212AB7475□G	X7R	4.7	±10, ±20	10	1.25+0.15/-0.05	R	150%	D	
16V	EMK212 B7105□G		X7R	1	±10, ±20	3.5	1.25±0.1	R/W	200%		
	EMK212 B7225□G		X7R	2.2	±10, ±20	10	1.25±0.1	R	150%		
	EMK212 B7475□G		X7R	4.7	±10, ±20	10	1.25±0.1	R	150%		
10V	LMK212 B7105□G		X7R	1	±10, ±20	3.5	1.25±0.1	R/W	200%		
	LMK212 B7225□G		X7R	2.2	±10, ±20	5	1.25±0.1	R	200%		
	LMK212 B7475□G		X7R	4.7	±10, ±20	10	1.25±0.1	R	150%		
	LMK212 B7106MG	LMK212AB7106MG	X7R	10	±20	10	1.25+0.15/-0.05	R	150%	D	
6.3V	JMK212 B7106□G	JMK212AB7106□G	X7R	10	±10, ±20	10	1.25+0.15/-0.05	R	150%		

· 0.85mm thickness (D)

Rated			Temp.	Canasitanas	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	Capacitance (µF)	tolerance (%)	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
16V	EMK212 B7474□D		X7R	0.47	±10, ±20	3.5	0.85±0.1	R/W	200%		
	EMK212 B7105□D		X7R	1	±10, ±20	5	0.85±0.1	R	200%		
	EMK212 B7225□D	EMK212AB7225□D	X7R	2.2	±10, ±20	5	0.85±0.1	R	150%		
10V	LMK212 B7105□D		X7R	1	±10, ±20	3.5	0.85±0.1	R	200%		
	LMK212 B7225□D	LMK212AB7225□D	X7R	2.2	±10, ±20	5	0.85±0.1	R	200%		
	LMK212 B7475□D	LMK212AB7475□D	X7R	4.7	±10, ±20	10	0.85±0.1	R	150%	R	

Capacitance tolerance code is applied to $\ \square$ of part number.

[Temperature Characteristic F: Y5V] ·1.25mm thickness(G)

Rated		Down mumb on 0	Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(μF)	tolerance (%)	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
50V	UMK212 F474ZG		F/Y5V	0.47	+80/-20	7	1.25±0.1	R/W	200%		
	UMK212 F105ZG		F/Y5V	1	+80/-20	7	1.25±0.1	R/W	200%		
16V	EMK212 F225ZG		F/Y5V	2.2	+80/-20	7	1.25±0.1	R/W	200%		
10V	LMK212 F475ZG		F/Y5V	4.7	+80/-20	9	1.25±0.1	R	200%		
	LMK212 F106ZG		F/Y5V	10	+80/-20	16	1.25±0.1	R	200%		
6.3V	JMK212 F106ZG		F/Y5V	10	+80/-20	16	1.25±0.1	R	200%		

· 0.85mm thickness (D)

Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(µF)	tolerance	(%)	(mm)	R:Reflow	% Rated	code	Note
voltage			Cilai.	(μι)	(%)	(70)	(IIIII)	W:Wave	voltage	(P/N 1)	
50V	UMK212 F224ZD		F/Y5V	0.22	+80/-20	7	0.85±0.1	R/W	200%		
10V	LMK212 F225ZD		F/Y5V	2.2	+80/-20	9	0.85±0.1	R	200%		
6.3V	JMK212 F475ZD		F/Y5V	4.7	+80/-20	16	0.85±0.1	R	200%		

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[Temperature Characteristic BJ: B/X5R]

* 1.6111111 11	nickness(L)			Ĭ			i e				
Rated	Part number 1	Part number 2	Temp.	Capacitance	Capacitance tolerance	tanδ	Thickness	Soldering R:Reflow	HALT % Rated	Internal code	Note
voltage	- arriamor i		char.	(μF)	(%)	(%)	(mm)	W:Wave	voltage	(P/N 1)	
50V	UMK316 BJ105□L		B/X5R*1	1	±10, ±20	3.5	1.6±0.2	R	200%		
	UMK316 BJ475□L		X5R	4.7	±10, ±20	10	1.6±0.2	R	150%		
25V	TMK316 BJ225□L		B/X5R*1	2.2	±10, ±20	3.5	1.6±0.2	R	200%		
	TMK316 BJ475□L		B/X5R	4.7	±10, ±20	5	1.6±0.2	R	150%		
	TMK316 BJ106□L		X5R*1	10	±10, ±20	5	1.6±0.2	R	150%		
16V	EMK316 BJ225□L		B/X5R*1	2.2	±10, ±20	3.5	1.6±0.2	R/W	200%		
	EMK316 BJ475□L		B/X5R	4.7	±10, ±20	5	1.6±0.2	R	200%		
	EMK316 BJ106□L		B/X5R*1	10	±10, ±20	5	1.6±0.2	R	150%		
	EMK316 BJ226ML	EMK316ABJ226ML	B/X5R	22	±20	10	1.6±0.2	R	150%		
10V	LMK316 BJ106□L		B/X5R*1	10	±10, ±20	5	1.6±0.2	R	200%		
	LMK316 BJ226ML	LMK316ABJ226ML	B/X5R	22	±20	10	1.6±0.2	R	150%		
	LMK316 BJ476ML	LMK316ABJ476ML	X5R	47	±20	10	1.6±0.2	R	150%		
6.3V	JMK316 BJ106□L		B/X5R*1	10	±10, ±20	5	1.6±0.2	R	200%		
	JMK316 BJ226□L	JMK316ABJ226□L	B/X5R	22	±10, ±20	10	1.6±0.2	R	200%		
	JMK316 BJ476ML	JMK316ABJ476ML	X5R	47	±20	10	1.6±0.2	R	200%		
	JMK316 BJ107ML	JMK316ABJ107ML	X5R	100	±20	10	1.6±0.2	R	150%		
4V	AMK316 BJ107ML	AMK316ABJ107ML	X5R	100	±20	10	1.6±0.2	R	150%		

Capacitance tolerance code is applied to □ of part number.

*1 We may provide X7R for some itemes according to the individual specification.

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■ REPRESENTATIVE PART NUMBERS

· 0.85mm thickness(D)

Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	(μF)	tolerance (%)	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
50V	UMK316 BJ105□D		B/X5R	1	±10, ±20	3.5	0.85±0.1	R	150%		
	UMK316 BJ225□D		B/X5R	2.2	±10, ±20	3.5	0.85±0.1	R	150%		
	UMK316 BJ475□D	UMK316ABJ475□D	X5R	4.7	±10, ±20	10	0.85±0.1	R	150%	D	
25V	TMK316 BJ105□D		B/X5R	1	±10, ±20	3.5	0.85±0.1	R	200%		
	TMK316 BJ225□D		B/X5R	2.2	±10, ±20	3.5	0.85±0.1	R	150%		
	TMK316 BJ475□D		X5R	4.7	±10, ±20	5	0.85±0.1	R	150%		
	TMK316 BJ106□D	TMK316ABJ106□D	X5R	10	±10, ±20	10	0.85±0.1	R	150%	D	
16V	EMK316 BJ225□D		B/X5R	2.2	±10, ±20	3.5	0.85±0.1	R	200%		
	EMK316 BJ475□D		B/X5R	4.7	±10, ±20	5	0.85±0.1	R	200%		
	EMK316 BJ106□D		X5R	10	±10, ±20	10	0.85±0.1	R	150%		
	EMK316 BJ226MD	EMK316ABJ226MD	X5R	22	±20	10	0.85±0.1	R	150%	D	
10V	LMK316 BJ475□D		B/X5R	4.7	±10, ±20	5	0.85±0.1	R	200%		
	LMK316 BJ106□D		B/X5R	10	±10, ±20	10	0.85±0.1	R	200%		
	LMK316 BJ226MD	LMK316ABJ226MD	X5R	22	±20	10	0.85±0.1	R	150%		
6.3V	JMK316 BJ106□D		B/X5R	10	±10, ±20	10	0.85±0.1	R	200%		
	JMK316 BJ226MD	JMK316ABJ226MD	X5R	22	±20	10	0.85±0.1	R	150%		
	JMK316 BJ476MD	JMK316ABJ476MD	X5R	47	±20	10	0.85±0.1	R	150%		

Capacitance tolerance code is applied to \square of part number.

[Temperature Characteristic B7:X7R]

Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance [µF]	Capacitance tolerance (%)	tanδ [%]	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
50V	UMK316 B7224□L		X7R	0.22	±10, ±20	2.5	1.6±0.2	R/W	200%		
	UMK316 B7474□L		X7R	0.47	±10, ±20	3.5	1.6±0.2	R/W	200%		
	UMK316 B7105□L		X7R	1	±10, ±20	3.5	1.6±0.2	R	200%		
	UMK316 B7225□L		X7R	2.2	±10, ±20	10	1.6±0.2	R	150%		
	UMK316 B7475□L	UMK316AB7475□L	X7R	4.7	±10, ±20	10	1.6±0.2	R	150%	D	
25V	TMK316 B7105□L		X7R	1	±10, ±20	3.5	1.6±0.2	R/W	200%		
	TMK316 B7225□L		X7R	2.2	±10, ±20	3.5	1.6±0.2	R	200%		
	TMK316 B7475□L	TMK316AB7475□L	X7R	4.7	±10, ±20	10	1.6±0.2	R	200%	D	
	TMK316 B7106□L	TMK316AB7106□L	X7R	10	±10, ±20	10	1.6±0.2	R	150%	D	
16V	EMK316 B7225□L		X7R	2.2	±10, ±20	3.5	1.6±0.2	R/W	200%		
	EMK316 B7106□L	EMK316AB7106□L	X7R	10	±10, ±20	10	1.6±0.2	R	200%	D	
10V	LMK316 B7225□L		X7R	2.2	±10, ±20	3.5	1.6±0.2	R/W	200%		
	LMK316 B7475□L		X7R	4.7	±10, ±20	5	1.6±0.2	R	200%		
	LMK316 B7106□L	LMK316AB7106□L	X7R	10	±10, ±20	10	1.6±0.2	R	200%	D	
	LMK316 B7226ML	LMK316AB7226ML	X7R	22	±20	10	1.6±0.2	R	150%	R	
6.3V	JMK316 B7106□L		X7R	10	±10, ±20	5	1.6±0.2	R	200%		

Capacitance tolerance code is applied to $\hfill\Box$ of part number.

[Temperature Characteristic F:F/Y5V]

·1.6mm thickness(L)

Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance [µF]	Capacitance tolerance (%)	tanδ [%]	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
35V	GMK316 F106ZL		F/Y5V	10	+80/-20	9	1.6±0.2	R	200%		
25V	TMK316 F106ZL		F/Y5V	10	+80/-20	9	1.6±0.2	R	200%		
16V	EMK316 F106ZL		F/Y5V	10	+80/-20	9	1.6±0.2	R	200%		
10V	LMK316 F226ZL		F/Y5V	22	+80/-20	16	1.6±0.2	R	200%		

•1.25mm thickness(G)

Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance (µF)	Capacitance tolerance (%)	tanδ (%)	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
50V	UMK316 F225ZG		F/Y5V	2.2	+80/-20	7	1.25±0.1	R/W	200%		
35V	GMK316 F475ZG		F/Y5V	4.7	+80/-20	7	1.25±0.1	R	200%		

· 0.85mm thickness (D)

Rated			Temp.	Canasitanas	Capacitance	4 5	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	char.	Capacitance	tolerance	tanδ [%]	(mm)	R:Reflow	% Rated	code	Note
voltage			Oriai.	(MI)	(%)	(70)	(IIIII)	W:Wave	voltage	(P/N 1)	
10V	LMK316 F475ZD		F/Y5V	4.7	+80/-20	9	0.85±0.1	R	200%		
6.3V	JMK316 F106ZD		F/Y5V	10	+80/-20	16	0.85±0.1	R	200%		

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Temperature Characteristic BJ:B/X5R]
•2.5mm thickness(M)

Detect			T	0	Capacitance	7 1	Thistory	Soldering	HALT	Internal	
Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance (µF)	tolerance (%)	tanδ (%)	Thickness (mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
50V	UMK325 BJ475MM		X5R	4.7	±20	5	2.5±0.2	R	150%		
	UMK325 BJ106MM		X5R	10	±20	5	2.5±0.2	R	150%		
25V	TMK325 BJ106MM		B/X5R*1	10	±20	3.5	2.5±0.2	R	150%		
16V	EMK325 BJ226MM		B/X5R	22	±20	5	2.5±0.2	R	150%		
	EMK325 BJ476MM		X5R	47	±20	10	2.5±0.2	R	150%		
10V	LMK325 BJ226MM		B/X5R	22	±20	5	2.5±0.2	R	200%		
	LMK325 BJ476MM		X5R	47	±20	10	2.5±0.2	R	150%		
	LMK325 BJ107MM	LMK325ABJ107MM	X5R	100	±20	10	2.5±0.3	R	150%		
6.3V	JMK325 BJ476MM		X5R	47	±20	10	2.5±0.2	R	150%		
	JMK325 BJ107MM	JMK325ABJ107MM	X5R	100	±20	10	2.5±0.3	R	150%		

·1.9mm thickness(Y, N)

Rated			T	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1	Part number 2	Temp. char.	Capacitance (μF)	tolerance (%)	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note
35V	GMK325 BJ225MN		B/X5R	2.2	±20	3.5	1.9±0.2	R	200%		
	GMK325 BJ475MN		X5R	4.7	±20	10	1.9±0.2	R	150%		
	GMK325 BJ106MN		B/X5R	10	±20	5	1.9±0.2	R	150%		
25V	TMK325 BJ335MN		B/X5R*1	3.3	±20	3.5	1.9±0.2	R	200%		
	TMK325 BJ475MN		B/X5R*1	4.7	±20	3.5	1.9±0.2	R	200%		
	TMK325 BJ106MN		B/X5R	10	±20	5	1.9±0.2	R	200%		
16V	EMK325 BJ475MN		B/X5R*1	4.7	±20	3.5	1.9±0.2	R	200%		
	EMK325 BJ106MN		B/X5R	10	±20	3.5	1.9±0.2	R	200%		
10V	LMK325 BJ226MY		B/X5R	22	±20	5	1.9+0.1/-0.2	R	150%		
	LMK325 BJ106MN		B/X5R*1	10	±20	3.5	1.9±0.2	R	200%		
6.3V	JMK325 BJ226MY		B/X5R	22	±20	5	1.9+0.1/-0.2	R	200%		
	JMK325 BJ107MY		X5R	100	±20	10	1.9+0.1/-0.2	R	150%		
	JMK325 BJ476MN		X5R	47	±20	10	1.9±0.2	R	150%		

· 0.85mm thickness (D)

Rated voltage	Part number 1	Part number 2	Temp. char.	Capacitance [µF]	Capacitance tolerance (%)	tanδ (%)	Thickness (mm)	Soldering R:Reflow W:Wave	HALT % Rated voltage	Internal code (P/N 1)	Note
25V	TMK325 BJ106MD		B/X5R	10	±20	5	0.85±0.1	R	150%		
16V	EMK325 BJ106MD		B/X5R	10	±20	5	0.85±0.1	R	150%		
	EMK325 BJ226MD		B/X5R	22	±20	10	0.85±0.1	R	150%		
10V	LMK325 BJ335MD		B/X5R	3.3	±20	3.5	0.85±0.1	R	200%		
	LMK325 BJ475MD		B/X5R	4.7	±20	5	0.85±0.1	R	200%		
	LMK325 BJ106MD		B/X5R	10	±20	5	0.85±0.1	R	150%		

^{*1} We may provide X7R for some itemes according to the individual specification.

【Temperature Characteristic B7 : X7R】 ·2.5mm thickness(M)

Rated			Tomp	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	
voltage	Part number 1 Part number 2 1 1 1 tolerance 1 1		(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	Note				
50V	UMK325 B7475MM		X7R	4.7	±20	5	2.5±0.2	R	150%		
25V	TMK325 B7226MM		X7R	22	±20	10	2.5±0.2	R	150%	R	
16V	EMK325 B7226MM		X7R	22	±20	10	2.5±0.2	R	150%	R	
10V	LMK325 B7476MM		X7R	47	±20	10	2.5±0.2	R	150%	R	
6.3V	JMK325 B7476MM		X7R	47	±20	10	2.5±0.2	R	200%	R	

·1.9mm thickness(Y, N)

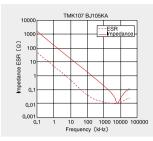
Rated			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	Note
voltage	Part number 1	Part number 2	char.	(μF)	tolerance (%)	(%)	(mm)	R:Reflow W:Wave	% Rated voltage	code (P/N 1)	
25V	TMK325 B7335MN		X7R	3.3	±20	3.5	1.9±0.2	R	200%		
	TMK325 B7475MN		X7R	4.7	±20	3.5	1.9±0.2	R	150%		
16V	EMK325 B7475MN		X7R	4.7	±20	3.5	1.9±0.2	R	200%		
	EMK325 B7106MN		X7R	10	±20	3.5	1.9±0.2	R	150%		
10V	LMK325 B7106MN		X7R	10	±20	3.5	1.9±0.2	R	200%		

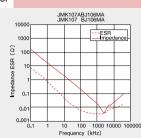
	Temperature on a action cite 1 117 101										
Rated voltage Part number			Temp.	Capacitance	Capacitance	tanδ	Thickness	Soldering	HALT	Internal	**
	Part number 1	Part number 2	char.	(µF)	tolerance	(%)	[mm]	R:Reflow % Rated Code	code	Note	
				.,,	(%)			W:Wave	voltage	(P/N 1)	
16V	EMK325 F226ZN		F/Y5V	22	+80/-20	16	1.9±0.2	R	200%		
10V	LMK325 F226ZN		F/Y5V	22	+80/-20	16	1.9±0.2	R	200%		
6.3V	JMK325 F476ZN		F/Y5V	47	+80/-20	16	1.9±0.2	R	200%		

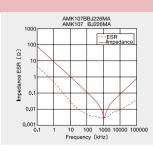
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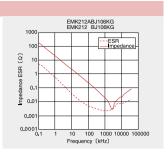
Example of Impedance ESR vs. Frequency characteristics

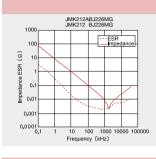
Taiyo Yuden multilayer ceramic capacitor

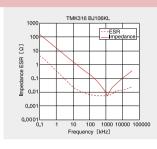


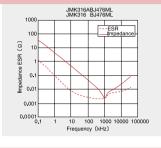


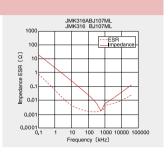


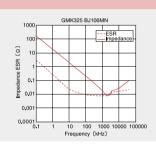


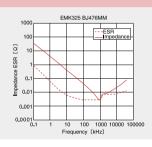


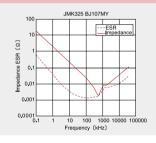


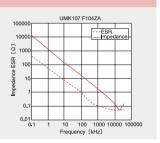


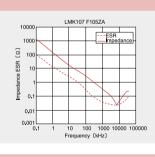




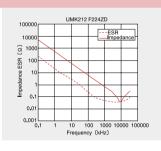


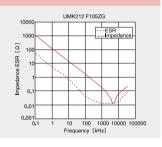


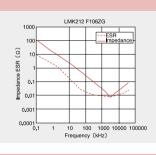


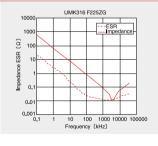


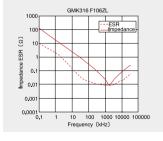


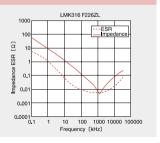


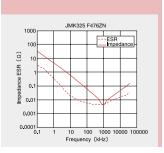












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1)Minimum Quantity

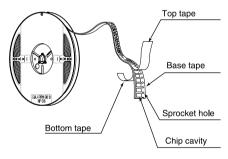
Taped package

	Thickness		Standard qu	uantity [pcs]		
Type	mm	code	Paper tape	Embossed tape		
☐MK042	0.2	С	_	40000		
☐MK063	0.3	P,T	15000			
□2K096	0.3	Р				
2N096	0.45	K	10000			
□WK105	0.3	Р	10000			
□WK103	0.5	V				
	0.2	С	20000	_		
☐MK105	0.3	Р	15000			
	0.5	V, W	40000			
□VK105	0.5	W	10000			
	0.45	K	4000			
☐MK107 ☐WK107	0.5	V	_	4000		
WICIO7	0.8	Α				
	0.5	V				
□2K110	0.6	В	4000			
	0.8	Α	4000	_		
	0.45	К	1			
☐MK212 ☐WK212	0.85	D				
□ VVI\Z IZ	1.25	G	_	3000		
□4K212	0.85	D				
□2K212	0.85	D	4000	_		
	0.85	D				
□MK316	1.15	F		3000		
□IVIN3 ID	1.25	G] —	3000		
	1.6	L	1			
	0.85	D				
	1.15	F	1	2000		
☐MK325	1.9	N] —			
	2.0max	Y	1			
	2.5	М		500(T), 1000(P)		
□MK432	2.5	М	_	500		

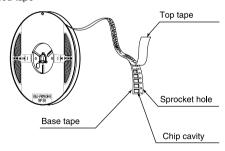
②Taping material

※No bottom tape for pressed carrier tape

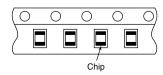
Paper tape

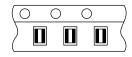


Embossed tape



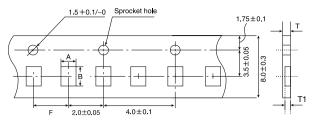
Chip filled





③Representative taping dimensions

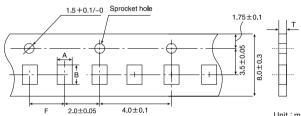
- Paper Tape (8mm wide)
- Pressed carrier tape (2mm pitch)



Unit: mm

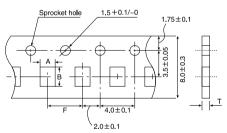
Type	Chip	Cavity	Insertion Pitch	Tape Thickness					
	Α	В	F	Т	T1				
☐MK063	0.37	0.67	2.0±0.05	0.45max.	0.42max.				
□WK105	0.65	1.15	2.0±0.05	0.45max.	0.42max.				

• Punched carrier tape (2mm pitch)



				Unit : mm	
Tuna	Chip (Cavity	Insertion Pitch	Tape Thickness	
Туре	Α	В	F	Т	
□2K096	0.72	1.02	2.0±0.05	0.45max. 0.6max.	
□MK105 □VK105	0.65	1.15	2.0±0.05	0.8max.	

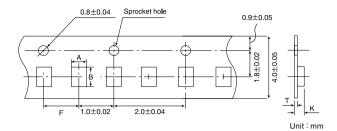
• Punched carrier tape (4mm pitch)



	`2.0±0.1 Unit : mm										
Tuno	Chip Cavity		Insertion Pitch	Tape Thickness							
Type	Α	В	F	Т							
□MK107 □WK107	1.0	1.8		1.1max.							
□2K110	1.15	1.55		1.0max.							
□MK212 □WK212	1.65	2.4	4.0±0.1								
□4K212 □2K212	1.00	2.4		1.1max.							
□MK316	2.0	3.6									

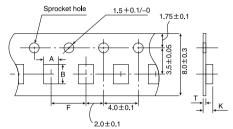
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Embossed tape (4mm wide)



Туре	Chip (Cavity	Insertion Pitch	Tape Thickness		
	Α	В	F	K	Т	
☐MK042	0.23	0.43	1.0±0.02	0.5max.	0.25max.	

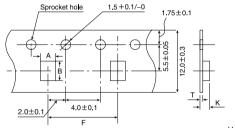
Embossed tape (8mm wide)



Unit: mm

Type	Chip	Cavity	Insertion Pitch	Tape Thickness		
туре	Α	В	F	K	Т	
□WK107	1.0	1.8		1.3max	0.25±0.1	
☐MK212	1.65	2.4	4.0+0.1	3.4max.		
☐MK316	2.0	3.6	4.0±0.1		0.6max.	
☐MK325	2.8	3.6			1	

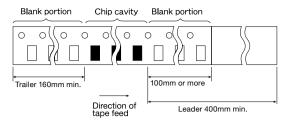
Embossed tape (12mm wide)



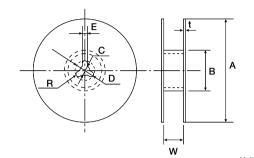
Unit: mm

Type	Chip (Cavity	Insertion Pitch	Tape Th	ickness		
Type	Α	В	F	K	Т		
□MK432	3.7	4.9	8.0±0.1	4.0max.	0.6max.		

4Trailer and Leader



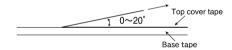
5Reel size



		Unit · mm
A	В	С
φ178±2.0	φ50min.	φ13.0±0.2
D	E	R
φ21.0±0.8	2.0±0.5	1.0
	t	W
4mm wide tape	1.5max.	5±1.0
8mm wide tape	2.5max.	10±1.5
12mm wide tape	2.5max.	14±1.5

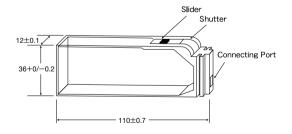
6Top Tape Strength

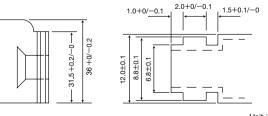
The top tape requires a peel-off force of 0.1 to 0.7N in the direction of the arrow as illustrated below.



7 Bulk Cassette

The exchange of individual specification is necessary. Please contact Taiyo Yuden sales channels.





Unit: mm

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2. Storage Conditions

Multilayer Ceramic Capacitors

Super Low Distortion Multilayer Ceramic Capacitors and Medium-High Voltage Multilayer Ceramic Capacitors are noted separately.

1.Operatir	1.Operating Temperature Range								
	Temperature Compensating	Standard	_		1405°C				
	(Class 1)	High Frequency Type	-55 to +125℃						
					Specification	Temperature Range			
			۱Г,		B −25 to +85°C				
Specified				BJ	X5R	-55 to +85°C			
Value	High Barraitti ita (Olasa O)		E	В7	X7R	-55 to +125°C			
	High Permittivity (Class 2)			26	X6S	-55 to +105°C			
				C7	X7S	-25 to +85°C -55 to +85°C -55 to +125°C			
				F	F	-25 to +85°C			
					Y5V	-30 to +85°C			

Specified Value	Temperature Compensating (Class 1)	Standard High Frequency Type	−55 to +125°C			
				Specification	Temperature Range	
			ВЈ	В	-25 to +85°C	
			BJ	X5R	-55 to +85°C	
	High Permittivity (Class 2)		B7	X7R	-55 to +125°C	
	High Fermittivity (Class 2)		C6	X6S	-55 to +105°C	
			C7	X7S	-55 to +125°C	
			F	F	-25 to +85°C	
				Y5V	-30 to +85°C	
	1					

3. Rated \	/oltage		
0		Standard	50VDC, 25VDC, 16VDC
Specified Value	(Class 1)	High Frequency Type	50VDC, 16VDC
	High Permittivity (Class 2)		50VDC, 35VDC, 25VDC, 16VDC, 10VDC, 6.3VDC, 4VDC

4. Withstanding Voltage (Between terminals)

Specified Value	Temperature Compensating	Standard	
	(Class 1)	High Frequency Type	No breakdown or damage
value	High Permittivity (Class 2)		

[Test Methods and Remarks]

I		Class 1	Class 2	
	Applied voltage	Rated voltage×3	Rated voltage×2.5	
	Duration	1 to 5	sec.	
1	Charge/discharge current	50mA max.		

	Applied voltage	riated voltage xo	riated voltage 12.5
	Duration	1 to 5	sec.
	Charge/discharge current	50mA	max.
-	<u> </u>	<u> </u>	<u> </u>
5	. Insulation Resistance		

Specified Value	Temperature Compensating	Standard	10000 MO min.	
	ed (Class 1)	High Frequency Type	10000 1012 111111.	
			C≤0.047 μ F : 10000 MΩ min. C>0.047 μ F : 500MΩ· μ F	
	, ,	riigir i diriittiy (diada 2) riata i		

[Test Methods and Remarks] Applied voltage: Rated voltage Duration: 60±5 sec. Charge/discharge current: 50mA max.

6.	Capacitance	(Tolerance)

o. capao.	tarroo (roiorarroo)							
Cassified	Temperature Compensating (Class 1)	Standard	$ \begin{array}{ c c c c c }\hline C & 0.5pF \leqq C \leqq 5pF : \pm 0.25pF \\ U \bigtriangleup & \hline 0.5pF \lessdot C \leqq 10pF : \pm 0.5pF \\ \hline C \gt 10pF : \pm 5\% \\ \hline \end{array} \begin{array}{ c c c c c c c c c c c c c c c c c c c$					
		High Frequency Type	CH 0.5pF≦C≦2pF: ±0.1pF C>2pF: ±5%					
	High Permittivity (Class 2)		BJ, B7, C6,C7: ±10% or ±20%, F: -20%/+80%					
	7							

[Test Methods and Remarks]

	Class 1		Class 2			
	Standard	Standard High Frequency Type		C>10µF		
Preconditioning	None		Thermal treatment (at 150°C for 1hr) Note 2			
Measuring frequency	1MHz	1MHz±10%		120±10Hz		
Measuring voltage Note 1	0.5 to 5Vrms		ge Note 1 0.5 to 5Vrms 1±0.2Vrms 0.5±0.1V		0.5±0.1Vrms	
Bias application		No	ne			

7. Q or Dissipation Factor

Specified Value	Temperature Compensating	Standard	C<30 pF:Q≥400+20C、C≥30 pF:Q≥1000	(C: Nominal capacitance)	
	(Class 1)	High Frequency Type	Refer to detailed specification		
	High Permittivity(Class 2) Note 1		BJ, B7, C6,C7: 2.5% max., F:7% max.		
<u></u>					

[Test Methods and Remarks]

	Class 1 Standard High Frequency Type		Class 2		
			C≦10 <i>μ</i> F	C>10µF	
Preconditioning	None		Thermal treatment (at 150°C for 1hr) Note 2		
Measuring frequency	1MHz±10%	1MHz±10% 1GHz		120±10Hz	
Measuring voltage Note 1	0.5 to 5Vrms		1±0.2Vrms	0.5±0.1Vrms	
Bias application		No	one		

High Frequency Type Measuring equipment: HP4291A Measuring jig: HP16192A

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8. Temperature Characteristic (Without voltage application)

	Temperature Compensating (Class 1)		Temp	erature (Charact	teristic [ppm/°C]	To	olerance		
		Standard	C	C□:0 CH, C		J, CK				
			R□	R□:-220 RH		H±60				
			S□	: -330	SH, S	SH, SJ, SK		J±120		
		High Frequency Type	Τ□	: -470	TJ, Tk	(K±250			
			U	: -750	UJ, U	J, UK				
			SL : +350 to -1000							
Specified		·								=
Value				Specifi	cation	Capacitance cha	ange	Reference to	emperature	
			BJ	В	3	±10%		20°	C	
			БЭ	X5	R	±15%		25°	C	Г

B7

C6

C7

F

[Test Methods and Remarks] Class 1

Capacitance at 20°C and 85°C shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation.

X7R

X6S

X7S

Y5V

±15%

±22%

±22%

+30/-80%

+22/-82%

$$\frac{(C_{85}-C_{20})}{C_{20}\times\triangle T}$$
 × 10⁶ (ppm/°C) $\triangle T$ =65

High Permittivity (Class 2)

Capacitance at each step shall be measured in thermal equilibrium, and the temperature characteristic shall be calculated from the following equation.

			_	
Step	B、F	X5R, X7R, X6S, X7S, Y5V	1	
1	Minimum operating temperature			
2	2 20°C 25°C			
3	Maximum operating temperature			

 $\frac{(C-C_2)}{C_2} \times 100(\%)$

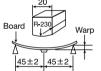
 $\begin{array}{ll} C & : Capacitance \ in \ Step \ 1 \ or \ Step \ 3 \\ C_2 & : Capacitance \ in \ Step \ 2 \end{array}$

9. Deflect	ion			
	Temperature Compensating	Standard		No abnormality Within $\pm 5\%$ or ± 0.5 pF, whichever is larger.
Specified Value	(Class 1)	High Frequency Type	Appearance : Capacitance change :	No abnormality Within±0.5 pF
	High Permittivity (Class 2)			No abnormality Within $\pm 12.5\%$ (BJ, B7, C6, C7), Within $\pm 30\%$ (F)

Test Methods and Remarks

Multilayer Ceramic Capacitors

	Board	Thickness	Warp	Duration
042、063 Type	along apovy ragin aubatrata	0.8mm	1mm	10 sec.
The other types	glass epoxy-resin substrate	1.6mm	1mm	10 Sec.



25°C

25℃

25°C

20°C

25℃

Capacitance measurement shall be conducted with the board bent

Temperature Range

-25 to +85°C -55 to +85°C

-55 to +125°C

-55 to +105°C

-55 to +125°C

-25 to +85°C

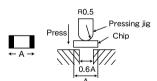
-30 to +85℃

Array Type

	Board	Thickness	Warp	Duration
096、110、212 Type	glass epoxy-resin substrate	1.6mm	1mm	10 sec.



[Test Methods and Remarks] High Frequency Type Applied force: 5N Duration: 10 sec.



11. Adhesive Strength of Terminal Electrodes

0	Temperature Compensating	Stariuaru	
Value	(Class 1)	High Frequency Type	No terminal separation or its indication.
	High Permittivity (Class 2)		

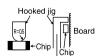
[Test Methods and Remarks]

Multilayer Ceramic Capacitors

	Applied force	Duration
042、063 Type	2N	30±5 sec.
105 Type or more	5N	30±5 sec.

Array Type

	Applied force	Duration
096 Type	2N	30±5 sec.
110、212 Type	5N	30±5 sec.



12. Solderability

0	Temperature Compensating	Standard		
Specified Value	(Class 1)	High Frequency Type	At least 95% of terminal electrode is covered by new solder.	
value	High Permittivity (Class 2)			

[Test Methods and Remarks]

Eutectic solder H60A or H63A 230±5	5°C 4±1 sec.
Lead-free solder Sn-3.0Ag-0.5Cu 245±3	3°C 4±1 sec.

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13. Resistance to Soldering No abnormality Within ±2.5% or ±0.25pF, whichever is larger. Appearance: Capacitance change: Q: Insulation resistance: Standard Initial value Initial value Withstanding voltage (between terminals): No abnormality Temperature Compensating (Class 1) Appearance: No abnormality Capacitance change: Within ±2.5% Specified High Frequency Type Q: Initial value Value Insulation resistance: Initial value Withstanding voltage (between terminals): No abnormality Appearance: Capacitance change: No abnormality Within ±7.5% (BJ, B7, C6, C7) Within ±20% (F) High Permittivity (Class 2) Note 1 Dissipation factor: Initial value Insulation resistance: Initial value Withstanding voltage (between terminals): No abnormality

[Test Methods and Remarks]

Class 1

0.000 /				
	042, 063 Type	105 Type Array (096, 110 Type)		
Preconditioning	None			
Preheating	150°C, 1 to 2 min.	80 to 100°C, 2 to 5 min. 150 to 200°C, 2 to 5 min.		
Solder temp.	270±5℃			
Duration	3±0.5 sec.			
Recovery	6 to 24 hrs (Standard condition) Note 5			

Class

Class 2					
	042、063 Type	105, 107, 212 Type Array(096, 110,212 Type)	316, 325 Type		
Preconditioning	Thermal	treatment (at 150°C for 1 hi	r) Note 2		
Preheating	150°C, 1 to 2 min.	80 to 100°C, 2 to 5 min. 150 to 200°C, 2 to 5 min.	80 to 100°C, 5 to 10 min. 150 to 200°C, 5 to 10 min.		
Solder temp.	270±5℃				
Duration	3±0.5 sec.				
Recovery	24±2 hrs (Standard condition) Note 5				

14. Temp	14. Temperature Cycle (Thermal Shock)					
	Temperature Compensating	Standard	Appearance: Capacitance change: Q: Insulation resistance: Withstanding voltage	No abnormality Within ±2.5% or ±0.25pF, whichever is larger. Initial value (between terminals): No abnormality		
Specified Value	(Class 1)	High Frequency Type	Appearance: Capacitance change: Q: Insulation resistance: Withstanding voltage	No abnormality Within ±0.25pF Initial value Initial value (between terminals): No abnormality		
	High Permittivity (Class 2) Note 1		Appearance: Capacitance change: Dissipation factor: Insulation resistance: Withstanding voltage	No abnormality Within ±7.5% (BJ, B7, C6, C7) Within ±20% (F) Initial value Initial value (between terminals): No abnormality		

[Test Methods and Remarks]

	Cla		Class 2		
Preconditioning	No	Thermal treatm	ent (at 150°C for	1 hr) Note 2	
	Step	Temperature	(°C)	Time(min.)	
	1	Lowest operating temperatur			
1 cycle	2	2 Normal temperature			
	3	3 Highest operating temperature +0/-3			
	4	Normal temperature		2 to 3	
Number of cycles	5 times				
Recovery	6 to 24 hrs (Standard condition) Note 5 24±2 hrs (Standard condition) Note 5				n) Note 5

15. Humidity (Steady State) Appearance: Capacitance change: Q: No abnormality Within ±5% or ±0.5pF, whichever is larger. C<10pF: Q≧200+10C Standard 10≦C<30pF: Q≥275+2.5C Temperature Compensating C≧30pF Q≧350 (C: Nominal capacitance) (Class 1) Insulation resistance: 1000 MΩ min. No abnormality Within ±0.5pF, Appearance: Specified Capacitance change: High Frequency Type . Value Insulation resistance: 1000 M Ω min. No abnormality Appearance: Capacitance change: Within ±12.5% (BJ, B7, C6, C7) Within ±30% (F) High Permittivity (Class 2) Note 1 Dissipation factor: 5.0% max. (BJ, B7, C6, C7) 11.0% max.(F) 50 MΩ μ F or 1000 M Ω whichever is smaller. Insulation resistance:

[Test Methods and Remarks]

Class 1

	Standard	High Frequency Type		
Preconditioning	None			
Temperature	40±2℃	60±2℃		
Humidity	90 to 95%RH			
Duration	500+24/-0 hrs			
Recovery	6 to 24 hrs (Standard condition) Note 5			

Class 2

	All items	
Preconditioning	Thermal treatment (at 150°C for 1 hr) Note	e 2
Temperature	40±2℃	
Humidity	90 to 95%RH	
Duration	500+24/-0 hrs	
Recovery	24±2 hrs (Standard condition) Note 5	

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16. Humi	6. Humidity Loading							
	Temperature Compensating (Class 1)	Standard	Appearance: Capacitance change: Q: Insulation resistance:	No abnormality Within $\pm 7.5\%$ or ± 0.75 pF, wh C <30 pF : Q $\ge 100+10$ C/3 C ≥ 30 pF : Q ≥ 200 500 M Ω min.	nichever is larger. (C: Nominal capacitance)			
		High Frequency Type	Appearance: Capacitance change: Insulation resistance:	C>2pF: Within ±0.75 pF	(C: Nominal capacitance)			
High Permittivity (Class 2)		ote 1	Appearance: Capacitance change: Dissipation factor: Insulation resistance:	Within ±30% (F) 5.0% max. (BJ, B7, C6, C7) 11.0% max. (F)				

[Test Methods and Remarks]

Standard	High Frequency Type		
	None		
40±2℃	60±2℃		
90 to 95%RH			
500+24/-0 hrs			
Rated voltage			
Charge/discharge current 50mA max.			
6 to 24 hrs (Standard condition) Note 5			
	40±2°C 90 t 500+ Rate		

Class Z	
	All items
Preconditioning	Voltage treatment (Rated voltage are applied for 1 hour at 40°C) Note 3
Temperature	40±2°C
Humidity	90 to 95%RH
Duration	500+24/-0 hrs
Applied voltage	Rated voltage
Charge/discharge current	50mA max.
Recovery	24±2 hrs (Standard condition) Note 5

17. High 7	Temperature Loading			
	Temperature Compensating (Class 1)	Standard	Appearance: Capacitance change: Q: Insulation resistance:	No abnormality Within $\pm 3\%$ or ± 0.3 pF, whichever is larger. $C < 1$ opF: $Q \ge 200 + 10C$ $10 \le C < 30$ pF: $Q \ge 275 + 2.5C$ $C \ge 30$ pF: $Q \ge 350$ ($C : Nominal capacitance$) $1000 M\Omega$ min.
		High Frequency Type	Appearance: Capacitance change: Insulation resistance:	No abnormality Within $\pm 3\%$ or ± 0.3 pF, whichever is larger. 1000 M Ω min.
	High Permittivity (Class 2) Note 1		Appearance: Capacitance change: Dissipation factor:	No abnormality Within ±12.5% (BJ, B7, C6, C7) Within ±30% (F) 5.0% max. (BJ, B7, C6, C7) 11.0% max. (F)
			Insulation resistance:	50 M Ω μF or 1000 M Ω , whichever is smaller.

[Test Methods and Remarks]

Class 1

	Standard	High Frequency Type			
Preconditioning	Preconditioning None				
Temperature	125±3℃				
Duration	Ouration 1000+48/-0 hrs				
Applied voltage	Rated voltage×2				
Charge/discharge current	50mA max.				
Recovery	6 to 24hr (Standard condition) Note 5				

Class 2

	BJ, F	BJ, F C6				
Preconditioning	Voltage treatment (Twice the rated voltage shall be applied for 1 hour at 85°C, 105°C or 125°C) Note 3, 4					
Temperature	85±2℃	85±2°C 105±3°C 125±3°				
Duration	1000+48/-0 hrs					
Applied voltage	Rated voltage×2 Note 4					
Charge/discharge current	50mA max.					
Recovery	24±2 hrs (Standard condition) Note 5					

Note 1 The figures indicate typical specifications. Please refer to individual specifications in detail.

Note 2 Thermal treatment: Initial value shall be measured after test sample is heat-treated at 150+0/-10°C for an hour and kept at room temperature for 24±2hours.

Note 3 Voltage treatment: Initial value shall be measured after test sample is voltage-treated for an hour at both the temperature and voltage specified in the test conditions, and

kept at room temperature for 24±2hours.

Note 4 Standard condition: Temperature: 5 to 35°C, Relative humidity: 45 to 85 % RH, Air pressure: 86 to 106kPa

When there are questions concerning measurement results, in order to provide correlation data, the test shall be conducted under the following condi-

Temperature: 20±2°C, Relative humidity: 60 to 70 % RH, Air pressure: 86 to 106kPa Unless otherwise specified, all the tests are conducted under the "standard condition".

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- Verification of operating environment, electrical rating and performance
 - 1. A malfunction of equipment in fields such as medical, aerospace, nuclear control, etc. may cause serious harm to human life or have severe social ramifications Therefore, any capacitors to be used in such equipment may require higher safety and reliability, and shall be clearly differentiated from them used in general purpose applications
- Precautions
- ◆Operating Voltage (Verification of Rated voltage)
 1. The operating voltage for capacitors must always be their rated voltage or less.
 - If an AC voltage is loaded on a DC voltage, the sum of the two peak voltages shall be the rated voltage or less.

 For a circuit where an AC or a pulse voltage may be used, the sum of their peak voltages shall also be the rated voltage or less.

 - 2. Even if an applied voltage is the rated voltage or less reliability of capacitors may be deteriorated in case that either a high frequency AC voltage or a pulse voltage having rapid rise time is used in a circuit.

2. PCB Design

- ◆Pattern configurations (Design of Land-patterns)

 1. When capacitors are mounted on PCBs, the amount of solder used (size of fillet) can directly affect the capacitor performance. Therefore, the following items must be carefully considered in the design of land patterns:
- (1) Excessive solder applied can cause mechanical stresses which lead to chip breaking or cracking. Therefore, please consider appropriate land-patterns for proper amount of solder.

 (2) When more than one component are jointly soldered onto the same land, each component's soldering point shall be separated by solder-resist.

 Pattern configurations (Capacitor layout on PCBs)

Precautions

After capacitors are mounted on boards, they can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering of the boards, etc.). For this reason, land pattern configurations and positions of capacitors shall be carefully considered to minimize stresses.

- ◆Pattern configurations (Design of Land-patterns)
- The following diagrams and tables show some examples of recommended land patterns to prevent excessive solder amounts.

 (1) Recommended land dimensions for typical chip capacitors
- - ●Multilayer Ceramic Capacitors: Recommended land dimensions (unit: mm) Wave-soldering

Тур	е	107	212	316	325
Size	L 1.6 2.0		3.2	3.2	
Size	W	0.8	1.25	1.6	2.5
Α		0.8 to 1.0	1.0 to 1.4	1.8 to 2.5	1.8 to 2.5
В		0.5 to 0.8	0.8 to 1.5	0.8 to 1.7	0.8 to 1.7
С		0.6 to 0.8	0.9 to 1.2	1.2 to 1.6	1.8 to 2.5

Reflow-soldering

Тур	е	042	063	105	107	212	316	325	432
Size	L	0.4	0.6	1.0	1.6	2.0	3.2	3.2	4.5
Size	W	0.2	0.3	0.5	0.8	1.25	1.6	2.5	3.2
Α		0.15 to 0.25	0.20 to 0.30	0.45 to 0.55	0.8 to 1.0	0.8 to 1.2	1.8 to 2.5	1.8 to 2.5	2.5 to 3.5
В		0.15 to 0.20	0.20 to 0.30	0.40 to 0.50	0.6 to 0.8	0.8 to 1.2	1.0 to 1.5	1.0 to 1.5	1.5 to 1.8
С	;	0.15 to 0.30	0.25 to 0.40	0.45 to 0.55	0.6 to 0.8	0.9 to 1.6	1.2 to 2.0	1.8 to 3.2	2.3 to 3.5

●LWDC: Recommended land dimensions for reflow-soldering (unit: mm)

Тур	ре	105	107	212
Size	L	0.52	0.8	1.25
Size	W	1.0	1.6	2.0
А		0.18 to 0.22	0.25 to 0.3	0.5 to 0.7
В	1	0.2 to 0.25	0.3 to 0.4	0.4 to 0.5
C	;	0.9 to 1.1	1.5 to 1.7	1.9 to 2.1

Technical considerations

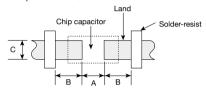
Array type:	Recommended land	dimensions f	or reflow-soldering	(unit: mm)
Thiray type.	necommended land	Ullile I SIOI IS I	or renow-soluening	(unit. iiiiii)

Typ	е	096 (2 circuits)	110 (2 circuits)	212 (2 circuits)	212 (4 circuits)
Size	L	0.9	1.37	2.0	2.0
	W	0.6	1.0	1.25	1.25
а		0.25 to 0.35	0.35 to 0.45	0.5 to 0.6	0.5 to 0.6
b		0.15 to 0.25	0.55 to 0.65	0.5 to 0.6	0.5 to 0.6
С		0.15 to 0.25	0.3 to 0.4	0.5 to 0.6	0.2 to 0.3
d		0.45	0.64	1.0	0.5

(2) Examples of good and bad solder application

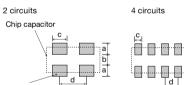
Items	Not recommended	Recommended
Mixed mounting of SMD and leaded components	Lead wire of component	Solder-resist
Component placement close to the chassis	Chassis Solder(for grounding)	Solder-resist
Hand-soldering of leaded components near mounted components	Lead wire of component- Soldering iron	Solder-resist
Horizontal component placement		Solder-resist

Land patterns for PCBs





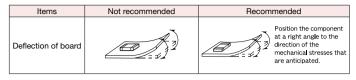




To next page

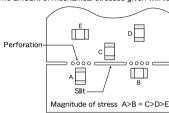
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- ◆Pattern configurations (Capacitor layout on PCBs)
- 1-1. The following is examples of good and bad capacitor layouts; capacitors shall be located to minimize any possible mechanical stresses from board warp or deflection.



Technical considerations

1-2. The amount of mechanical stresses given will vary depending on capacitor layout. Please refer to diagram below.



1-3. When PCB is split, the amount of mechanical stress on the capacitors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, please consider the PCB, split methods as well as chip location.

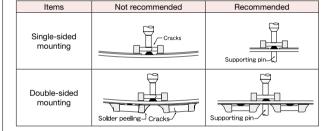
3. Mounting

- Adjustment of mounting machine
 - When capacitors are mounted on PCB, excessive impact load shall not be imposed on them. 2. Maintenance and inspection of mounting machines shall be conducted periodically

Precautions

- Selection of Adhesives
 - 1. When chips are attached on PCBs with adhesives prior to soldering, it may cause capacitor characteristics degradation unless the following factors are appropriately checked: size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, please contact us for further information.
- ◆Adjustment of mounting machine
- 1. When the bottom dead center of a pick-up nozzle is too low, excessive force is imposed on capacitors and causes damages. To avoid this, the following points shall be considerable
 - (1) The bottom dead center of the pick-up nozzle shall be adjusted to the surface level of PCB without the board deflection.

 - (2) The pressure of nozzle shall be adjusted between 1 and 3 N static loads.
 (3) To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins shall be used on the other side of the PCB. The following diagrams show some typical examples of good and bad pick-up nozzle placement:



Technical considerations

- As the alignment pin is worn out, adjustment of the nozzle height can cause chipping or cracking of capacitors because of mechanical impact on the capacitors. To avoid this, the monitoring of the width between the alignment pins in the stopped position, maintenance, check and replacement of the pin shall be conducted periodically.
- Selection of Adhesives

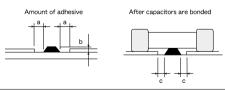
Some adhesives may cause IR deterioration. The different shrinkage percentage of between the adhesive and the capacitors may result in stresses on the capacitors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect components. Therefore, the following precautions shall be noted in the application of adhesives.

- (1) Required adhesive characteristics
 - a. The adhesive shall be strong enough to hold parts on the board during the mounting & solder process.
 b. The adhesive shall have sufficient strength at high temperatures.

 - c. The adhesive shall have good coating and thickness consistency. d. The adhesive shall be used during its prescribed shelf life. e. The adhesive shall harden rapidly.

 - f. The adhesive shall have corrosion resistance
 - g. The adhesive shall have excellent insulation characteristics.
 - h. The adhesive shall have no emission of toxic gasses and no effect on the human body.
- (2) The recommended amount of adhesives is as follows;

[Recommended condition]		
Figure	212/316 case sizes as examples	
а	0.3mm min	
b	100 to 120 μm	
С	Adhesives shall not contact land	



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Selection of Flux

Since flux may have a significant effect on the performance of capacitors, it is necessary to verify the following conditions prior to use;

- (1) Flux used shall be less than or equal to 0.1 wt% (in CI equivalent) of halogenated content. Flux having a strong acidity content shall not be applied. (2) When shall capacitors are soldered on boards, the amount of flux applied shall be controlled at the optimum level.
- (3) When water-soluble flux is used, special care shall be taken to properly clean the boards.

Precautions

◆Solderina

Temperature, time, amount of solder, etc. shall be set in accordance with their recommended conditions.

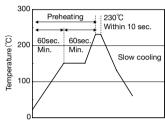
Sn-Zn solder paste can adversely affect MLCC reliability. Please contact us prior to usage of Sn-Zn solder

- 1-1. When too much halogenated substance (Chlorine, etc.) content is used to activate flux, or highly acidic flux is used, it may lead to corrosion of terminal electrodes or degradation of insulation resistance on the surfaces of the capacitors.
- 1-2. Flux is used to increase solderability in wave soldering. However if too much flux is applied, a large amount of flux gas may be emitted and may adversely affect the solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system.
- 1-3. Since the residue of water-soluble flux is easily dissolved in moisture in the air, the residues on the surfaces of capacitors in high humidity conditions may cause a degradation of insulation resistance and reliability of the capacitors. Therefore, the cleaning methods and the capability of the machines used shall also be considered carefully when water-soluble flux is used

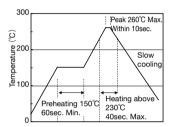
- Ceramic chip capacitors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling.
- Therefore, the soldering must be conducted with great care so as to prevent malfunction of the components due to excessive thermal shock
- Preheating: Capacitors shall be preheated sufficiently, and the temperature difference between the capacitors and solder shall be within 100 to 130°C.
 Cooling: The temperature difference between the capacitors and cleaning process shall not be greater than 100°C.

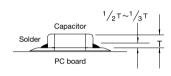
[Reflow soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]





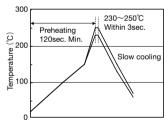
Caution

- (i) The ideal condition is to have solder mass (fillet) controlled to 1/2 to 1/3 of the thickness of a capacitor.
- ②Because excessive dwell times can adversely affect solderability, soldering duration shall be kept as
- close to recommended times as possible.

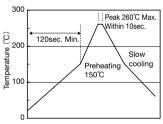
Technical considerations

[Wave soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]

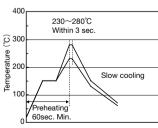


Caution

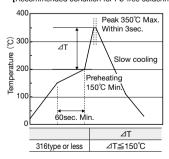
①Wave soldering must not be applied to capacitors designated as for reflow soldering only.

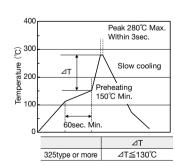
[Hand soldering]

[Recommended conditions for eutectic soldering]



[Recommended condition for Pb-free soldering]





- ①Use a 50W soldering iron with a maximum tip diameter of 1.0 mm.
- ②The soldering iron shall not directly touch capacitors

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Precautions

◆Cleaning conditions

- 1. When PCBs are cleaned after capacitors mounting, please select the appropriate cleaning solution in accordance with the intended use of the cleaning. (e.g. to
- remove soldering flux or other materials from the production process.)

 2. Cleaning condition shall be determined after it is verified by using actual cleaning machine that the cleaning process does not affect capacitor's characteristics.
- 1. The use of inappropriate cleaning solutions can cause foreign substances such as flux residue to adhere to capacitors or deteriorate their outer coating, resulting in a degradation of the capacitor's electrical properties (especially insulation resistance)

Technical considerations

2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may adversely affect the performance of the capacitors.

In the case of ultrasonic cleaning, too much power output can cause excessive vibration of PCBs which may lead to the cracking of capacitors or the soldered portion, or decrease the terminal electrodes' strength. Therefore, the following conditions shall be carefully checked;

Ultrasonic output : 20 W/ ℓ or less Ultrasonic frequency: 40 kHz or less Ultrasonic washing period : 5 min. or less

6. Resin coating and mold

1. With some type of resins, decomposition gas or chemical reaction vapor may remain inside the resin during the hardening period or while left under normal storage conditions resulting in the deterioration of the capacitor's performance. 2. When a resin's hardening temperature is higher than capacitor's operating temperature, the stresses generated by the excessive heat may lead to damage or

destruction of capacitors

The use of such resins, molding materials etc. is not recommended

7. Handling

Precautions

- ◆Splitting of PCB

 1. When PCBs are split after components mounting, care shall be taken so as not to give any stresses of deflection or twisting to the board.
 - 2. Board separation shall not be done manually, but by using the appropriate devices

Precautions

Mechanical considerations

Be careful not to subject capacitors to excessive mechanical shocks.

- (1) If ceramic capacitors are dropped onto a floor or a hard surface, they shall not be used.
- (2) Please be careful that the mounted components do not come in contact with or bump against other boards or components

8. Storage conditions

◆Storage
1.To maintain the solderability of terminal electrodes and to keep packaging materials in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible.

Recommended conditions

Ambient temperature Below 30°C

Precautions

Below 70% RH Humidity

The ambient temperature must be kept below 40°C. Even under ideal storage conditions, solderability of capacitor is deteriorated as time passes, so capacitors shall be used within 6 months from the time of delivery.

· Ceramic chip capacitors shall be kept where no chlorine or sulfur exists in the air.

2. The capacitance values of high dielectric constant capacitors will gradually decrease with the passage of time, so care shall be taken to design circuits . Even if capacitance value decreases as time passes, it will get back to the initial value by a heat treatment at 150°C for 1hour.

Technical considerIf capacitors are stored in a high temperature and humidity environment, it might rapidly cause poor solderability due to terminal oxidation and quality loss of taping/ packaging materials. For this reason, capacitors shall be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the capacitors.

*RCR-2335B (Safety Application Guide for fixed ceramic capacitors for use in electronic equipment) is published by JEITA.

Please check the guide regarding precautions for deflection test, soldering by spot heat, and so on.

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